

LTspice IV Library API [Ver 4.12r]

9/20/2011

[njf.asy](#)

spice_model: NJF

Required Refdes Prefix: JN

Description: N-Channel JFET

Spice_Node_Sequence: [1] D , [2] G , [3] S ,

[FerriteBead.asy](#)

Required Refdes Prefix: L_Ferrite_Bead

Description: A Ferrite Bead

Spice_Node_Sequence: [1] A , [2] B ,

[res.asy](#)

spice_model: R

Required Refdes Prefix: R

Description: A resistor

Spice_Node_Sequence: [1] A , [2] B ,

[npn2.asy](#)

spice_model: NPN

Required Refdes Prefix: QN

Description: Bipolar NPN transistor

Spice_Node_Sequence: [1] C , [2] B , [3] E ,

[load2.asy](#)

spice_model: I

Required Refdes Prefix: I

Description: Current Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,

[csw.asy](#)

spice_model: CSW

Required Refdes Prefix: W

Description: Current controlled switch

Spice_Node_Sequence: [1] + , [2] - ,

[nmos.asy](#)

spice_model: NMOS

Required Refdes Prefix: MN

Description: N-Channel MOSFET transistor

Spice_Node_Sequence: [1] D , [2] G , [3] S ,

[voltage.asy](#)

spice_model: V

Required Refdes Prefix: V

Description: Voltage Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,

[ind.asy](#)

spice_model: L

Required Refdes Prefix: L

Description: Inductor

Spice_Node_Sequence: [1] A , [2] B ,

[bi2.asy](#)

spice_model: I=F(...)

Required Refdes Prefix: B

Description: Arbitrary behavioral current source

Spice_Node_Sequence: [1] + , [2] - ,

[polcap.asy](#)

spice_model: C

Required Refdes Prefix: C

Description: Polarized Capacitor

Spice_Node_Sequence: [1] A , [2] B ,

[sw.asy](#)

spice_model: SW

Required Refdes Prefix: S

Description: Voltage controlled switch

Spice_Node_Sequence: [1] A , [2] B , [3] NC+ , [4] NC- ,

[pjf.asy](#)

spice_model: PJF

Required Refdes Prefix: JP

Description: P-Channel JFET transistor

Spice_Node_Sequence: [1] D , [2] G , [3] S ,

[bv.asy](#)

spice_model: V=F(...)

Required Refdes Prefix: B

Description: Arbitrary behavioral voltage source

Spice_Node_Sequence: [1] + , [2] - ,

[tline.asy](#)

spice_model: Td=50n Z0=50

Required Refdes Prefix: T

Description: Ideal Lossless Transmission Line

Spice_Node_Sequence: [1] I1 , [2] R1 , [3] I2 , [4] R2 ,

[pnp2.asy](#)

spice_model: PNP

Required Refdes Prefix: QP

Description: Bipolar PNP transistor

Spice_Node_Sequence: [1] C , [2] B , [3] E ,

[load.asy](#)

spice_model: I

Required Refdes Prefix: I

Description: Current Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] A , [2] B ,

[FerriteBead2.asy](#)

Required Refdes Prefix: L_Ferrite_Bead

Description: A Ferrite Bead(Alternate symbol)

Spice_Node_Sequence: [1] A , [2] B ,

[nnp4.asy](#)

spice_model: NPN

Required Refdes Prefix: QN

Description: Bipolar NPN transistor with substrate node. You would normally use this with your own .model statement.

Spice_Node_Sequence: [1] C , [2] B , [3] E , [4] S ,

[ltline.asy](#)

spice_model: LTRA

Required Refdes Prefix: O

Description: Lossy Transmission Line. Note: You must supply the .model for this device

Spice_Node_Sequence: [1] I1 , [2] R1 , [3] I2 , [4] R2 ,

[pmos4.asy](#)

spice_model: PMOS

Required Refdes Prefix: MP

Description: P-Channel MOSFET transistor with explicit substrate connection(used for monolithic MOSFETS)

Spice_Node_Sequence: [1] D , [2] G , [3] S , [4] B ,

[LED.asy](#)

spice_model: D

Required Refdes Prefix: D

Description: Light Emitting Diode

Spice_Node_Sequence: [1] + , [2] - ,

[pnp.asy](#)

spice_model: PNP

Required Refdes Prefix: QP

Description: Bipolar PNP transistor

Spice_Node_Sequence: [1] C , [2] B , [3] E ,

[npn.asy](#)

spice_model: NPN

Required Refdes Prefix: QN

Description: Bipolar NPN transistor

Spice_Node_Sequence: [1] C , [2] B , [3] E ,

[cap.asy](#)

spice_model: C

Required Refdes Prefix: C

Description: Capacitor

Spice_Node_Sequence: [1] A , [2] B ,

[e.asy](#)

spice_model: E

Required Refdes Prefix: E

Description: Voltage dependent voltage source

Spice_Node_Sequence: [1] + , [2] - , [3] P , [4] N ,

[mesfet.asy](#)

spice_model: NMF

Required Refdes Prefix: Z

Description: GaAs MESFET transistor Note: You must supply the .model for this device

Spice_Node_Sequence: [1] D , [2] G , [3] S ,

[npn3.asy](#)

spice_model: NPN

Required Refdes Prefix: QN

Description: Bipolar NPN transistor

Spice_Node_Sequence: [1] C , [2] B , [3] E ,

[schottky.asy](#)

spice_model: D

Required Refdes Prefix: D

Description: Schottky diode

Spice_Node_Sequence: [1] + , [2] - ,

[varactor.asy](#)

spice_model: D

Required Refdes Prefix: D

Description: Diode

Spice_Node_Sequence: [1] + , [2] - ,

[diode.asy](#)

spice_model: D

Required Refdes Prefix: D

Description: Diode

Spice_Node_Sequence: [1] + , [2] - ,

[nmos4.asy](#)

spice_model: NMOS

Required Refdes Prefix: MN

Description: N-Channel MOSFET transistor with explicit substrate connection(used for monolithic MOSFETS)

Spice_Node_Sequence: [1] D , [2] G , [3] S , [4] B ,

[pnp4.asy](#)

spice_model: PNP

Required Refdes Prefix: QP

Description: Bipolar PNP transistor with substrate node. You would normally use this with your own .model statement.

Spice_Node_Sequence: [1] C , [2] B , [3] E , [4] S ,

[g.asy](#)

spice_model: G

Required Refdes Prefix: G

Description: Voltage dependent current source

Spice_Node_Sequence: [1] + , [2] - , [3] NC+ , [4] NC- ,

[res2.asy](#)

spice_model: R

Required Refdes Prefix: R

Description: A resistor

Spice_Node_Sequence: [1] A , [2] B ,

[current.asy](#)

spice_model: I

Required Refdes Prefix: I

Description: Current source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,

[zener.asy](#)

spice_model: D

Required Refdes Prefix: D

Description: Zener Diode

Spice_Node_Sequence: [1] + , [2] - ,

[pmos.asy](#)

spice_model: PMOS

Required Refdes Prefix: MP

Description: P-Channel MOSFET transistor

Spice_Node_Sequence: [1] D , [2] G , [3] S ,

[ind2.asy](#)

spice_model: L

Required Refdes Prefix: L

Description: Inductor

Spice_Node_Sequence: [1] A , [2] B ,

[lpnp.asy](#)

spice_model: LPNP

Required Refdes Prefix: QP

Description: Bipolar Lateral PNP transistor. You would normally use this with your own .model statement.

Spice_Node_Sequence: [1] C , [2] B , [3] E , [4] S ,

[f.asy](#)

spice_model: F

Required Refdes Prefix: F

Description: Linear current dependent current source

Spice_Node_Sequence: [1] + , [2] - ,

[h.asy](#)

spice_model: H

Required Refdes Prefix: H

Description: Linear current dependent voltage source

Spice_Node_Sequence: [1] + , [2] - ,

[bi.asy](#)

spice_model: I=F(...)

Required Refdes Prefix: B

Description: Arbitrary behavioral current source

Spice_Node_Sequence: [1] + , [2] - ,

[g2.asy](#)

spice_model: G

Required Refdes Prefix: G

Description: Voltage dependent current source

Spice_Node_Sequence: [1] + , [2] - , [3] NC+ , [4] NC- ,

[e2.asy](#)

spice_model: E

Required Refdes Prefix: E

Description: Voltage dependent voltage source

Spice_Node_Sequence: [1] + , [2] - , [3] P , [4] N ,

[LTC2909-50.asy](#)

spice_model: LTC2909-50

Required Refdes Prefix: X

Description: Dual Adjustable UV/OV Monitor, 5.0V UVLO

spice_lib: LTC2909-50.sub

Spice_Node_Sequence: [1] GND , [2] REF , [3] ADJ2 , [4] ADJ1 , [5] SEL , [6] TMR , [7] Vcc , [8] _RST ,

[LTC4413.asy](#)

spice_model: LTC4413

Required Refdes Prefix: X

spice_lib: LTC4413.sub

Description: Dual 2.6A, 2.5V to 5.5V Ideal Diodes in 3mm×3mm DFN

Spice_Node_Sequence: [1] INA , [2] ENBA , [3] GND , [4] ENBB , [5] INB , [6] OUTB , [7] STAT , [8] OUTA ,

[LTC1727-2.5.asy](#)

spice_model: LTC1727-2.5

Required Refdes Prefix: X

spice_lib: LTC1727-2.5.sub

Description: µPower Precision Triple Supply Monitor in 8-Lead MSOP

Spice_Node_Sequence: [1] Vcc3 , [2] Vcc2.5 , [3] VccA , [4] GND , [5] CompA , [6] _RST , [7] Comp2.5 , [8] Comp3 ,

[LTC6993-1.asy](#)

spice_model: LTC6993-1

Required Refdes Prefix: X

spice_lib: LTC6993-1.sub

Description: TimerBlox Non-Retriggerable One Shot

Spice_Node_Sequence: [1] TRIG , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC1727-5.asy](#)

spice_model: LTC1727-5

Required Refdes Prefix: X

spice_lib: LTC1727-5.sub

Description: μ Power Precision Triple Supply Monitor in 8-Lead MSOP

Spice_Node_Sequence: [1] Vcc3 , [2] Vcc5 , [3] VccA , [4] GND , [5] CompA , [6] _RST , [7] Comp5 , [8] Comp3 ,

[LT4356-3.asy](#)

spice_model: LT4356-3

spice_lib: LT4356-3.sub

Description: Surge Stopper

Required Refdes Prefix: X

Spice_Node_Sequence: [1] TMR , [2] FB , [3] OUT , [4] Gate , [5] SNS , [6] Vcc , [7] _SHDN , [8] _FLT , [9] EN , [10] GND , [11] Aout , [12] IN+ ,

[LTC2909-33.asy](#)

spice_model: LTC2909-33

Required Refdes Prefix: X

Description: Dual Adjustable UV/OV Monitor, 3.3V UVLO

spice_lib: LTC2909-33.sub

Spice_Node_Sequence: [1] GND , [2] REF , [3] ADJ2 , [4] ADJ1 , [5] SEL , [6] TMR , [7] Vcc , [8] _RST ,

[LTC1728-1.8.asy](#)

spice_model: LTC1728-1.8

Required Refdes Prefix: X

spice_lib: LTC1728-1.8.sub

Description: μ Power Precision Triple Supply Monitor in 5 lead SOT-23

Spice_Node_Sequence: [1] _RST , [2] GND , [3] VccA , [4] Vcc1.8 , [5] Vcc3 ,

[LTC5505-1.asy](#)

spice_model: LTC5505-1

Required Refdes Prefix: X

spice_lib: LTC5505-1.sub

Description: RF Power Detector with Buffered Output and >40dB Dynamic Range

Spice_Node_Sequence: [1] RFin , [2] GND , [3] _SHDN , [4] Vout , [5] Vcc ,

[modulate2.asy](#)

Required Refdes Prefix: A

spice_lib: MODULATOR

Description: Behavioral VCO and Modulator. Output is a sine wave modulated by AM and FM input voltages. This version supplies an output in quadrature.

Spice_Node_Sequence: [1] FM , [2] AM , [3] COS , [4] SIN , [5] com ,

[LTC6992-4.asy](#)

spice_model: LTC6992-4

Required Refdes Prefix: X

spice_lib: LTC6992-4.sub

Description: TimerBlox 5% TO 100% PWM

Spice_Node_Sequence: [1] MOD , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC4355.asy](#)

spice_model: LTC4355

Required Refdes Prefix: X

spice_lib: LTC4355

Description: Positive High Voltage Ideal Diode-OR with Input Supply and Fuse Monitors

Spice_Node_Sequence: [1] Mon1 , [2] Mon2 , [3] GND , [4] Set , [5] _FuseFlt2 , [6] _FuseFlt1 , [7] _VdsFlt , [8] Out , [9] Gate2 , [10] In2 , [11] Gate1 , [12] In1 , [13] _PwrFlt1 , [14] _PwrFlt2 ,

[LTC6994-2.asy](#)

spice_model: LTC6994-2

Required Refdes Prefix: X

spice_lib: LTC6994-2.sub

Description: TimerBlox Retriggerable Either Edge Delay

Spice_Node_Sequence: [1] IN , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,
[LTC4358.asy](#)

spice_model: LTC4358

Required Refdes Prefix: X

spice_lib: LTC4358.sub

Description: 5A Ideal Diode

Spice_Node_Sequence: [1] OUT , [2] IN , [3] Drain , [4] Gate , [5] Vdd , [6] GND ,
[LT4356-2.asy](#)

spice_model: LT4356-2

spice_lib: LT4356-2.sub

Description: Surge Stopper

Required Refdes Prefix: X

Spice_Node_Sequence: [1] TMR , [2] FB , [3] OUT , [4] Gate , [5] SNS , [6] Vcc , [7] _SHDN , [8] _FLT ,
[9] EN , [10] GND , [11] Aout , [12] IN+ ,

[varistor.asy](#)

Required Refdes Prefix: A

spice_lib: VARISTOR

Description: Voltage controlled varistor

Spice_Node_Sequence: [1] invin , [2] noninvin , [3] out , [4] com ,
[capmeter.asy](#)

spice_model: capometer

Required Refdes Prefix: X

Description: A vector impedance meter. You must .lib capometer.sub

Spice_Node_Sequence: [1] DUT+ , [2] DUT- , [3] bias , [4] Resistance , [5] Capacitance ,
[LTC6908-1.asy](#)

spice_model: LTC6908-1

Required Refdes Prefix: X

spice_lib: LTC6908-1.sub

Description: Resistor Set SOT-23 Oscillator with Spread Spectrum Modulation

Spice_Node_Sequence: [1] V+ , [2] GND , [3] SET , [4] MOD , [5] OUT2 , [6] OUT1 ,
[LTC1968.asy](#)

spice_model: LTC1968

Required Refdes Prefix: X

spice_lib: LTC1968.lib

Description: Precision Wide Bandwidth RMS-to-DC Converter

Spice_Node_Sequence: [1] GND , [2] In1 , [3] In2 , [4] Vout , [5] OutRtn , [6] Vdd ,
[LTC4352.asy](#)

spice_model: LTC4352

spice_lib: LTC4352.sub

Description: Low Voltage Ideal Diode Controller with Monitoring

Required Refdes Prefix: X

Spice_Node_Sequence: [1] Vin , [2] Vcc , [3] UV , [4] OV , [5] _Status , [6] _Fault , [7] Rev , [8] Out , [9]
GND , [10] CPO , [11] Gate , [12] Source ,

[LTC1728-5.asy](#)

spice_model: LTC1728-5

Required Refdes Prefix: X

spice_lib: LTC1728-5.sub

Description: μ Power Precision Triple Supply Monitor in 5-lead SOT-23

Spice_Node_Sequence: [1] _RST , [2] GND , [3] VccA , [4] Vcc5 , [5] Vcc3 ,
[LTC6992-3.asy](#)

spice_model: LTC6992-3

Required Refdes Prefix: X

spice_lib: LTC6992-3.sub

Description: TimerBlox 0% TO 95% PWM

Spice_Node_Sequence: [1] MOD , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,
[LTC6993-3.asy](#)

spice_model: LTC6993-3

Required Refdes Prefix: X

spice_lib: LTC6993-3.sub

Description: TimerBlox Non-Retrig Fall Edge One Shot

Spice_Node_Sequence: [1] TRIG , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,
[LTC1696.asy](#)

spice_model: LTC1696

Required Refdes Prefix: X

spice_lib: LTC1696.sub

Description: Overvoltage Protection Controller

Spice_Node_Sequence: [1] FB1 , [2] GND , [3] Vcc , [4] Out , [5] FB2 , [6] _Timer/Reset ,
[LTC6994-1.asy](#)

spice_model: LTC6994-1

Required Refdes Prefix: X

spice_lib: LTC6994-1.sub

Description: TimerBlox Retriggerable One Edge Delay

Spice_Node_Sequence: [1] IN , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,
[LTC6993-4.asy](#)

spice_model: LTC6993-4

Required Refdes Prefix: X

spice_lib: LTC6993-4.sub

Description: TimerBlox Retriggerable One Shot

Spice_Node_Sequence: [1] TRIG , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,
[LTC4362-1.asy](#)

spice_model: LTC4362-1

Required Refdes Prefix: X

spice_lib: LTC4362-1.sub

Description: 1.2A Overvoltage/Overcurrent Protector

Spice_Node_Sequence: [1] GP , [2] In , [3] Sense , [4] Out , [5] _On , [6] _Pwrgd , [7] Gnd ,
[LTC1967.asy](#)

spice_model: LTC1967

Required Refdes Prefix: X

spice_lib: LTC1967.lib

Description: Precision Extended Bandwidth RMS-to-DC Converter

Spice_Node_Sequence: [1] GND , [2] In1 , [3] In2 , [4] Vout , [5] OutRtn , [6] Vdd ,
[LTC4413-1.asy](#)

spice_model: LTC4413-1

Required Refdes Prefix: X

spice_lib: LTC4413-1.sub

Description: Dual 2.6A, 2.5V to 5.5V Fast Ideal Diodes in 3mm×3mm DFN

Spice_Node_Sequence: [1] INA , [2] ENBA , [3] GND , [4] ENBB , [5] INB , [6] OUTB , [7] STAT , [8] OUTA ,

[LTC4300A-1.asy](#)

spice_model: LTC4300A-1

Required Refdes Prefix: X

spice_lib: LTC4300A-1.sub

Description: Hot Swappable 2-Wire Bus Buffers Note: Stop Bit and Bus Idle detection not modeled

Spice_Node_Sequence: [1] ENABLE , [2] SCLOUT , [3] SCLIN , [4] GND , [5] READY , [6] SDAIN , [7]

SDAOUT , [8] Vcc ,

[LTC4361-2.asy](#)

spice_model: LTC4361-2

Required Refdes Prefix: X

spice_lib: LTC4361-2.sub

Description: Overvoltage/Overcurrent Protection Controller

Spice_Node_Sequence: [1] GP , [2] In , [3] Sense , [4] Gate , [5] _On , [6] Out , [7] _Pwrgd , [8] Gnd ,

[LTC4365.asy](#)

spice_model: LTC4365

Required Refdes Prefix: X

spice_lib: LTC4365.sub

Description: UV, OV and Reverse Supply Protection Controller

Spice_Node_Sequence: [1] GND , [2] OV , [3] UV , [4] VIN , [5] GATE , [6] VOUT , [7] _FAULT , [8] _SHDN ,

[LTC6993-2.asy](#)

spice_model: LTC6993-2

Required Refdes Prefix: X

spice_lib: LTC6993-2.sub

Description: TimerBlox Retriggerable One Shot

Spice_Node_Sequence: [1] TRIG , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC6992-1.asy](#)

spice_model: LTC6992-1

Required Refdes Prefix: X

spice_lib: LTC6992-1.sub

Description: TimerBlox 0% to 100% DC PWM

Spice_Node_Sequence: [1] MOD , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC4416-1.asy](#)

spice_model: LTC4416-1

Required Refdes Prefix: X

spice_lib: LTC4416-1.sub

Description: 36V, Low Loss Dual PowerPath Controller for Large PFETs

Spice_Node_Sequence: [1] H1 , [2] E1 , [3] GND , [4] _E2 , [5] _H2 , [6] G2 , [7] V2 , [8] Vs , [9] V1 , [10] G1 ,

[LTC4265.asy](#)

spice_model: LTC4265

Required Refdes Prefix: X

spice_lib: LTC4265.sub

Description: IEEE802.3at High Power PD Interface Controller with 2-event Classification

Spice_Node_Sequence: [1] SHDN , [2] _T2PSE , [3] RCLASS , [4] Vin , [5] Vout , [6] PWRGD , [7] _PWRGD , [8] AGND ,

[LTC6992-2.asy](#)

spice_model: LTC6992-2

Required Refdes Prefix: X

spice_lib: LTC6992-2.sub

Description: TimerBlox 5% TO 95% PWM

Spice_Node_Sequence: [1] MOD , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[modulate.asy](#)

Required Refdes Prefix: A

spice_lib: MODULATOR

Description: Behavioral VCO and Modulator. Output is a sine wave modulated by AM and FM input voltages.

Spice_Node_Sequence: [1] FM , [2] AM , [3] Q , [4] com ,

[LTC2954-2.asy](#)

spice_model: LTC2954-2

Required Refdes Prefix: X

spice_lib: LTC2954-2.sub

Description: Push Button On/Off Controller with μ P Interrupt

Spice_Node_Sequence: [1] Vin , [2] _PB , [3] ONT , [4] GND , [5] _INT , [6] _EN , [7] PDT , [8] _KILL ,
[LTC2909-25.asy](#)

spice_model: LTC2909-25

Required Refdes Prefix: X

Description: Dual Adjustable UV/OV Monitor, 2.5V UVLO

spice_lib: LTC2909-25.sub

Spice_Node_Sequence: [1] GND , [2] REF , [3] ADJ2 , [4] ADJ1 , [5] SEL , [6] TMR , [7] Vcc , [8] _RST ,

[LTC4416.asy](#)

spice_model: LTC4416

Required Refdes Prefix: X

spice_lib: LTC4416.sub

Description: 36V, Low Loss Dual PowerPath Controller for Large PFETs

Spice_Node_Sequence: [1] H1 , [2] E1 , [3] GND , [4] _E2 , [5] _H2 , [6] G2 , [7] V2 , [8] Vs , [9] V1 , [10] G1 ,

[LTC5505-2.asy](#)

spice_model: LTC5505-2

Required Refdes Prefix: X

spice_lib: LTC5505-2.sub

Description: RF Power Detector with Buffered Output and >40dB Dynamic Range

Spice_Node_Sequence: [1] RFin , [2] GND , [3] _SHDN , [4] Vout , [5] Vcc ,

[LTC1966.asy](#)

spice_model: LTC1966

Required Refdes Prefix: X

spice_lib: LTC1966.lib

Description: Precision μ Power, Delta-Sigma RMS-to-DC Converter

Spice_Node_Sequence: [1] GND , [2] In1 , [3] In2 , [4] Vss , [5] Vout , [6] OutRtn , [7] Vdd ,

[LTC4411.asy](#)

spice_model: LTC4411

Required Refdes Prefix: X

spice_lib: LTC4411.sub

Description: 2.6A Low Loss Ideal diode in ThinSOT

Spice_Node_Sequence: [1] Vin , [2] GND , [3] CTL , [4] STAT , [5] OUT ,

[LTC6991.asy](#)

spice_model: LTC6991

Required Refdes Prefix: X

spice_lib: LTC6991.sub

Description: TimerBlox Low Freq Clock

Spice_Node_Sequence: [1] RST , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC6908-2.asy](#)

spice_model: LTC6908-2

Required Refdes Prefix: X

spice_lib: LTC6908-2.sub

Description: Resistor Set SOT-23 Oscillator with Spread Spectrum Modulation

Spice_Node_Sequence: [1] V+ , [2] GND , [3] SET , [4] MOD , [5] OUT2 , [6] OUT1 ,

[LTC4362-2.asy](#)

spice_model: LTC4362-2

Required Refdes Prefix: X

spice_lib: LTC4362-2.sub

Description: 1.2A Overvoltage/Overcurrent Protector

Spice_Node_Sequence: [1] GP , [2] In , [3] Sense , [4] Out , [5] _On , [6] _Pwrgd , [7] Gnd ,
[LTC4413-2.asy](#)

spice_model: LTC4413-2

Required Refdes Prefix: X

spice_lib: LTC4413-2.sub

Description: Dual 2.6A, 2.5V to 5.5V Fast Ideal Diodes in 3mm×3mm DFN

Spice_Node_Sequence: [1] INA , [2] ENBA , [3] GND , [4] ENBB , [5] INB , [6] OUTB , [7] OVP , [8] OVI , [9] STAT , [10] OUTA ,

[LTC6900.asy](#)

spice_model: LTC6900

Required Refdes Prefix: X

spice_lib: LTC6900.sub

Description: Resistor Set SOT-23 Oscillator

Spice_Node_Sequence: [1] V+ , [2] GND , [3] SET , [4] DIV , [5] OUT ,

[LTC1728-2.5.asy](#)

spice_model: LTC1728-2.5

Required Refdes Prefix: X

spice_lib: LTC1728-2.5.sub

Description: uPower Precidiom Triple Supply Monitor in 5-lead SOT-23

Spice_Node_Sequence: [1] _RST , [2] GND , [3] VccA , [4] Vcc2.5 , [5] Vcc3 ,

[LTC2954-1.asy](#)

spice_model: LTC2954-1

Required Refdes Prefix: X

spice_lib: LTC2954-1.sub

Description: Push Button On/Off Controller with μ P Interrupt

Spice_Node_Sequence: [1] Vin , [2] _PB , [3] ONT , [4] GND , [5] _INT , [6] EN , [7] PDT , [8] _KILL ,

[LTC1728-3.3.asy](#)

spice_model: LTC1728-3.3

Required Refdes Prefix: X

spice_lib: LTC1728-3.3.sub

Description: μ Power Precision Triple Supply Monitor in 5 lead SOT-23

Spice_Node_Sequence: [1] _RST , [2] GND , [3] VccA , [4] Vcc1.8 , [5] Vcc3 ,

[LT6650.asy](#)

spice_model: LT6650

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power, 400mV Reference with Rail-to-Rail Buffer Amplifier in SOT-23

Spice_Node_Sequence: [1] IN , [2] GND , [3] OUT , [4] FB ,

[LTC4357.asy](#)

spice_model: LTC4357

Required Refdes Prefix: X

spice_lib: LTC4357.sub

Description: Positive High Voltage Ideal Diode Controller

Spice_Node_Sequence: [1] OUT , [2] IN , [3] Gate , [4] GND , [5] Vdd ,

[LTC4360-2.asy](#)

spice_model: LTC4360-2

Required Refdes Prefix: X

spice_lib: LTC4360-2.sub

Description: Overvoltage Protection Controller

Spice_Node_Sequence: [1] In , [2] Gate , [3] Gp , [4] Out , [5] _Pwrgd , [6] Gnd ,

[LT1166.asy](#)

spice_model: LT1166

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Power Output Stage Automatic Bias System

Spice_Node_Sequence: [1] Vtop , [2] Vin , [3] Vout , [4] Vbottom , [5] Sense- , [6] Ilim- , [7] Ilim+ , [8] Sense+ ,

[LTC6990.asy](#)

spice_model: LTC6990

Required Refdes Prefix: X

spice_lib: LTC6990.sub

Description: TimerBlox SOT-23 VCO

Spice_Node_Sequence: [1] OE , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LT1739.asy](#)

spice_model: LT1739

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Dual 500mA, 200MHz xDSL Line Driver Amplifier(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] S , [7] SR ,

[LTC4414.asy](#)

spice_model: LTC4414

Required Refdes Prefix: X

spice_lib: LTC4414.sub

Description: 36V, Low Loss PowerPath Controller for Large PFETs

Spice_Node_Sequence: [1] STAT , [2] CTL , [3] GND , [4] Sense , [5] Vin , [6] Gate ,

[LTC4360-1.asy](#)

spice_model: LTC4360-1

Required Refdes Prefix: X

spice_lib: LTC4360-1.sub

Description: Overvoltage Protection Controller

Spice_Node_Sequence: [1] In , [2] Gate , [3] _On , [4] Out , [5] _Pwrgd , [6] Gnd ,

[LTC2952.asy](#)

spice_model: LTC2952

Required Refdes Prefix: X

Description: Dual Ideal Diode Push Button Power Path Controller with Supervisor

spice_lib: LTC2952

Spice_Node_Sequence: [1] VM , [2] PFI , [3] WDE , [4] _PB , [5] _RST , [6] _PFO , [7] ONT , [8] OFFT , [9] GND , [10] _INT , [11] EN , [12] G2 , [13] V2 , [14] VS , [15] V1 , [16] G1 , [17] G1Stat , [18] M2 , [19] M1 , [20] _KILL ,

[LTC4361-1.asy](#)

spice_model: LTC4361-1

Required Refdes Prefix: X

spice_lib: LTC4361-1.sub

Description: Overvoltage/Overcurrent Protection Controller

Spice_Node_Sequence: [1] GP , [2] In , [3] Sense , [4] Gate , [5] _On , [6] Out , [7] _Pwrgd , [8] Gnd ,

[LT1794.asy](#)

spice_model: LT1794

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Dual 500mA, 200MHz xDSL Line Driver Amplifier(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] S , [7] SR ,

[LT4356-1.asy](#)

spice_model: LT4356-1

spice_lib: LT4356-1.sub

Description: Surge Stopper

Required Refdes Prefix: X

Spice_Node_Sequence: [1] TMR , [2] FB , [3] OUT , [4] Gate , [5] SNS , [6] Vcc , [7] _SHDN , [8] _FLT , [9] EN , [10] GND , [11] Aout , [12] IN+ ,
[sample.asy](#)

Required Refdes Prefix: A

spice_lib: SAMPLEHOLD

Description: Behavioral Sample and Hold function block

Spice_Node_Sequence: [1] in+ , [2] in- , [3] CLK , [4] S/H , [5] out , [6] com ,

[LTC6406.asy](#)

spice_model: LTC6406

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: 3GHz, Low Noise, Rail-to-Rail Differential Amplifier/Driver\n\nNote: Distortion is not modeled.

Spice_Node_Sequence: [1] IN- , [2] Vocm , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] _SHDN , [8] IN+ , [9] OUTF+ , [10] OUTF- ,

[LT1795.asy](#)

spice_model: LT1795

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 500mA/50MHz Current Feedback Amplifier/xDSL Line Driver

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDNREF , [7] SHDN , [8] COMP ,

[LTC6104.asy](#)

spice_model: LTC6104

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: High Voltage, High Side, Bi-Directional Current Sense Amplifier

Spice_Node_Sequence: [1] OUT , [2] V- , [3] INB+/Vs , [4] INB- , [5] INA- , [6] INA+ ,

[LTC6252.asy](#)

spice_model: LTC6252

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: 720MHz, 3.5mA, R_R I/O Op Amp

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 ,

[LTC6246.asy](#)

spice_model: LTC6246

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: 180MHz, 1mA, R_R I/O Op Amp

Spice_Node_Sequence: [1] 1 , [2] 4 , [3] 3 , [4] 2 , [5] 9 , [6] 7 ,

[LT1190.asy](#)

spice_model: LT1190

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra High Speed Operational Amplifier($A_v \geq 1$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1999-20.asy](#)

spice_model: LT1999-20

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: High Voltage, Bidirectional Current Sense Amplifier

Spice_Node_Sequence: [1] V+ , [2] +IN , [3] -IN , [4] GND , [5] Ref , [6] Vout , [7] _SHDN ,
[LT1466.asy](#)

spice_model: LT1466

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT6013.asy](#)

spice_model: LT6013

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 145 μ A, 9.5nV/rtHz, $A_v > 5$, Rail-to-Rail Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT1169.asy](#)

spice_model: LT1169

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Low Noise, Picoampere Bias Current, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1367.asy](#)

spice_model: LT1367

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1800.asy](#)

spice_model: LT1800

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 80MHz, 25V/ μ s Low Power Rail-to-Rail Input and Output Precision Op Amp

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1498.asy](#)

spice_model: LT1498

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 10MHz, 6V/ μ s, Rail-to-Rail Input and Output Precision C-Load Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1225.asy](#)

spice_model: LT1225

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Very High Speed Operational Amplifier($A_v \geq 5$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1219.asy](#)

spice_model: LT1219

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT1810.asy](#)

spice_model: LT1810

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 180MHz, 350V/ μ s, Rail-to-Rail Input and Output, Low Distortion, Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1497.asy](#)

spice_model: LT1497

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 125mA, 50MHz, Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1191.asy](#)

spice_model: LT1191

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1361.asy](#)

spice_model: LT1361

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 50MHz, 800V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6202.asy](#)

spice_model: LT6202

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 100 MHz, Rail-to-Rail, 1.9nV/rtHz Low Power Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6402-6.asy](#)

spice_model: LT6402-6

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 300MHz Low Distortion, Low Noise Differential Amplifier/ADC Driver

Spice_Node_Sequence: [1] VccC , [2] VOcm , [3] VccA , [4] VeeA , [5] +OUT , [6] +OUTFILT , [7] -OUTFILT , [8] -OUT , [9] VeeB , [10] VccB , [11] _EN , [12] VeeC , [13] -INB , [14] -INA , [15] +INB , [16] +INA ,

[LT1056A.asy](#)

spice_model: LT1056A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC6088.asy](#)

spice_model: LTC6088

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: QUAD 14MHz, Rail-to-Rail, CMOS Amplifiers

Spice_Node_Sequence: [1] 3 , [2] 2 , [3] 7 , [4] 4 , [5] 1 ,

[LTC2052.asy](#)

spice_model: LTC2052

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad Zero-Drift Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6553.asy](#)

spice_model: LT6553

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: 650MHz Gain of 2 Triple Video Amplifier

Spice_Node_Sequence: [1] _EN , [2] DGND , [3] INR , [4] AGND1 , [5] ING , [6] AGND2 , [7] INB , [8] V- , [9] OUTB , [10] V+ , [11] OUTG , [12] OUTR ,

[LT1816.asy](#)

spice_model: LT1816

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 220MHz, 1500V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT2079A.asy](#)

spice_model: LT2079A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6000.asy](#)

spice_model: LT6000

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 1.8V, 13 μ A Precision Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT , [6] SHDN_L ,

[LT1467.asy](#)

spice_model: LT1467

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1227.asy](#)

spice_model: LT1227

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 140MHz Video Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1179A.asy](#)

spice_model: LT1179A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 17mA Max, Quad Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6550.asy](#)

spice_model: LT6550

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 3.3V Triple Video Amplifier

Spice_Node_Sequence: [1] IN , [2] GND , [3] VCC , [4] VEE , [5] OUT ,

[LTC2050.asy](#)

spice_model: LTC2050

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, Zero-Drift Operational Amplifier in SOT-23

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT6207.asy](#)

spice_model: LT6207

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad Single Supply 3V, 100MHz Video Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,
[LT1012.asy](#)

spice_model: LT1012

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Picoamp Input Current, μ Volt Offset, Low noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT6235.asy](#)

spice_model: LT6235

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 60MHz, Rail-to-Rail Output, 1.9nV/rtHz, 1.2mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,
[LT1079.asy](#)

spice_model: LT1079

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Quad, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1352.asy](#)

spice_model: LT1352

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 250mA, 3MHz, 200V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT6222.asy](#)

spice_model: LT6222

Required Refdes Prefix: X

spice_lib: LTC4.LIB

Description: Quad 60MHz, 20V/us, Low Power, RRIO Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT1260.asy](#)

spice_model: LT1260

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Cost Triple 130MHz Current Feedback Amplifier with Shutdown

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LTC6082.asy](#)

spice_model: LTC6082

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: Quad μ Power Precision, CMOS Rail-to-Rail Operational Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LTC1052.asy](#)

spice_model: LTC1052

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Chopper-Stabilized Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1056S8.asy](#)

spice_model: LT1056S8

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC2054.asy](#)

spice_model: LTC2054

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Low Power Zero-Drift Op Amp in SOT-23

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1328.asy](#)

spice_model: LT1328

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 4Mbps IrDA Infrared Receiver -- simulate with LT1328PD, the IR diode

Spice_Node_Sequence: [1] IN , [2] FIL , [3] SW , [4] GND , [5] DATA , [6] VCC , [7] MODE , [8] Vbias ,

[LT6014.asy](#)

spice_model: LT6014

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 145 μ A, 9.5nV/rtHz, $A_v > 5$, Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1782.asy](#)

spice_model: LT1782

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power Over-The-Top, SOT-23, Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[Lt1206.asy](#)

spice_model: LT1206

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 250mA/60MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] COMP ,

[LT1193.asy](#)

spice_model: LT1193

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Video Difference Amplifier, Adjustable Gain

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] REF , [7] FB ,

[LT1056.asy](#)

spice_model: LT1056

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC6405.asy](#)

spice_model: LTC6405

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: 2.7GHz, 5V, Low Noise, Rail-to-Rail Input Differential Input/Output Amplifier/Driver\n\nNote: Distortion is not modeled.

Spice_Node_Sequence: [1] IN- , [2] Vocm , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] _SHDN , [8] IN+ , [9] OUTF- , [10] OUTF+ ,

[LT1207.asy](#)

spice_model: LT1207

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 250mA/60MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] COMP ,

[LT1078.asy](#)

spice_model: LT1078

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Dual, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1807.asy](#)

spice_model: LT1807

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 325MHz, Dual Rail-to-Rail Input and Output, Low Distortion, Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1801.asy](#)

spice_model: LT1801

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 80MHz, 25V/ μ s Low Power Rail-to-Rail Input and Output Precision Op Amp(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC6081.asy](#)

spice_model: LTC6081

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: Dual μ Power Precision, CMOS Rail-to-Rail Operational Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1413.asy](#)

spice_model: LT1413

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Single Supply, Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC1100.asy](#)

spice_model: LTC1100

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, Chopper-Stabilized Instrumentation Amplifier

Spice_Node_Sequence: [1] GND , [2] CMRR , [3] In- , [4] V- , [5] V+ , [6] In+ , [7] Comp , [8] Out ,

[LT1817.asy](#)

spice_model: LT1817

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 220MHz, 1500V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1128.asy](#)

spice_model: LT1128

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra-Low Noise Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC2053.asy](#)

spice_model: LTC2053

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: Precision, Rail-to-Rail, Zero-Drift Instrumentation Amplifier with Resistor-Programmable Gain

Spice_Node_Sequence: [1] _E , [2] -IN , [3] +IN , [4] V- , [5] REF , [6] RG , [7] OUT , [8] V+ ,

[LT1465.asy](#)

spice_model: LT1465

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, 1MHz C-Load Picoampere Bias Current JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1365.asy](#)

spice_model: LT1365

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 70MHz, 1000V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1179.asy](#)

spice_model: LT1179

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 17mA Max, Quad Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1057.asy](#)

spice_model: LT1057

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual JFET Input Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1113.asy](#)

spice_model: LT1113

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Low Noise, Precision, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1010.asy](#)

spice_model: LT1010

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Fast +/-150mA Power Buffer

Spice_Node_Sequence: [1] In , [2] V+ , [3] V- , [4] Bias , [5] OUT ,

[LT1814.asy](#)

spice_model: LT1814

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 3mA, 100MHz, 750V/ μ s Operational Amplifier(1 of 4)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1499.asy](#)

spice_model: LT1499

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 10MHz, 6V/ μ s, Rail-to-Rail Input and Output Precision C-Load Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1212.asy](#)

spice_model: LT1212

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 14MHz, 7V/ μ s, Single Supply Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1395.asy](#)

spice_model: LT1395

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 400MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC2051.asy](#)

spice_model: LTC2051

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual Zero-Drift Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] SHDN_L ,

[LTC1051A.asy](#)

spice_model: LTC1051A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Zero-Drift Operational Amplifier with Internal Capacitors

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC6404-4.asy](#)

spice_model: LTC6404-4

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: 600MHz, Low Noise, High Precision Fully Differential Input/Output Amplifier/Driver
Note: Distortion is not modeled.

Spice_Node_Sequence: [1] IN- , [2] Vocm , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] _SHDN , [8] IN+ , [9] OUTF+ , [10] OUTF- ,

[LT1006S8.asy](#)

spice_model: LT1006S8

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Single Supply Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1102.asy](#)

spice_model: LT1102

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: High Speed, Precision, JFET Input Instrumentation Amplifier(Fixed Gain = 10 or 100)

Spice_Node_Sequence: [1] GND , [2] Tap1 , [3] In- , [4] V- , [5] V+ , [6] In+ , [7] Tap2 , [8] Out ,
[LT1001.asy](#)

spice_model: LT1001

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LTC6400-26.asy](#)

spice_model: LTC6400-26

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: 1.3GHz Low Noise, Low Distortion Differential ADC Driver for DC-140MHz IF\n\nNote:
Distortion is not modeled.

Spice_Node_Sequence: [1] IN- , [2] Vocm , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] _EN , [8] IN+ , [9]
OUTF- , [10] OUTF+ ,

[LT1127.asy](#)

spice_model: LT1127

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Decompensated Low Noise High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1007.asy](#)

spice_model: LT1007

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low noise, High Speed Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1209.asy](#)

spice_model: LT1209

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 45MHz, 400V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LTC6910-3.asy](#)

spice_model: LTC6910-3

Required Refdes Prefix: X

spice_lib: LTC1.LIB

Description: Digitally Controlled Programmable Gain Amplifiers in SOT-23

Spice_Node_Sequence: [1] OUT , [2] AGND , [3] IN , [4] V- , [5] G0 , [6] G1 , [7] G2 , [8] V+ ,
[LTC6078.asy](#)

spice_model: LTC6078

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: Dual μ Power Precision, CMOS Rail-to-Rail Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT1221.asy](#)

spice_model: LT1221

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Very High Speed Operational Amplifier($A_v \geq 4$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1253.asy](#)

spice_model: LT1253

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Low Cost Video Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC1150.asy](#)

spice_model: LTC1150

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: +-15V Zero-Drift Operational Amplifier with Internal Capacitors

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1636.asy](#)

spice_model: LT1636

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Over-The-Top μ Power Rail-to-Rail Input and Output Operational Amplifier with Shutdown

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1252.asy](#)

spice_model: LT1252

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Cost Video Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6002.asy](#)

spice_model: LT6002

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: Quad 1.8V, 13 μ A Precision Rail-to-Rail Op Amps

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6100.asy](#)

spice_model: LT6100

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Precision, Gain Selectable High Side Current Sense Amplifier

Spice_Node_Sequence: [1] VS- , [2] VCC , [3] FIL , [4] VEE , [5] VOUT , [6] A2 , [7] A4 , [8] VS+ ,

[LT1226.asy](#)

spice_model: LT1226

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Very High Speed Operational Amplifier($A_v \geq 25$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1464.asy](#)

spice_model: LT1464

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, 1MHz C-Load Picoampere Bias Current JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC6087.asy](#)

spice_model: LTC6087

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: Dual 14MHz, Rail-to-Rail, CMOS Amplifiers

Spice_Node_Sequence: [1] 3 , [2] 2 , [3] 7 , [4] 4 , [5] 1 ,

[LTC1052CS.asy](#)

spice_model: LTC1052CS

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Chopper-Stabilized Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1804.asy](#)

spice_model: LT1804

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 100V/ μ s, 85MHz Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1812.asy](#)

spice_model: LT1812

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 325MHz, Single Rail-to-Rail Input and Output, Low Distortion, Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LTC1992-1.asy](#)

spice_model: LTC1992-1

Required Refdes Prefix: X

spice_lib: LTC4.LIB

Description: Low Power, Fully Differential I/O Amplifier

Spice_Node_Sequence: [1] IN- , [2] VOCM , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] VMID , [8] IN+ ,

[LT1723.asy](#)

spice_model: LT1723

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 200MHz Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6233-10.asy](#)

spice_model: LT6233-10

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 375MHz, AV=10, Rail-to-Rail Output, 1.9nV/rtHz, 1.2mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] EN_L ,

[LT1815.asy](#)

spice_model: LT1815

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 220MHz, 1500V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6005.asy](#)

spice_model: LT6005

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: Quad 1.6V, 1 μ A Precision Rail-to-Rail Input and Output Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1637.asy](#)

spice_model: LT1637

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 1.1MHz, 0.4V/ μ s Over-The-Top μ Power Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT1178.asy](#)

spice_model: LT1178

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 17mA Max, Dual Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT6012.asy](#)

spice_model: LT6012

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 135 μ A, 14nV/rtHz, Rail-to-Rail Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,
[LT1014D.asy](#)

spice_model: LT1014D

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1097.asy](#)

spice_model: LT1097

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Cost, Low Power Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1996.asy](#)

spice_model: LT1996

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Precision, 100 μ A Gain Selectable Amplifier

Spice_Node_Sequence: [1] P9 , [2] P27 , [3] P81 , [4] Vee , [5] Ref , [6] OUT , [7] VCC , [8] M81 , [9] M27 , [10] M9 ,
[LT1014A.asy](#)

spice_model: LT1014A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1077.asy](#)

spice_model: LT1077

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1112.asy](#)

spice_model: LT1112

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Low Power Precision, Picoamp Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1028CS.asy](#)

spice_model: LT1028CS

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra Low Noise Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1364.asy](#)

spice_model: LT1364

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 70MHz, 1000V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LTC6254.asy](#)

spice_model: LTC6254

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: Quad, 720MHz, 3.5mA, R-R I/O Op Amp

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 ,
[LT1001A.asy](#)

spice_model: LT1001A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1006.asy](#)

spice_model: LT1006

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Single Supply Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1078A.asy](#)

spice_model: LT1078A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Dual, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1884.asy](#)

spice_model: LT1884

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Rail-to-Rail Output pA Input Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1167.asy](#)

spice_model: LT1167

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Single Resistor Gain Programmable, Precision Instrumentation Amplifier

Spice_Node_Sequence: [1] Rg , [2] In- , [3] In+ , [4] V- , [5] Ref , [6] Out , [7] V+ , [8] Rg ,
[LT1490.asy](#)

spice_model: LT1490

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1467L.asy](#)

spice_model: LT1467L

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1993-4.asy](#)

spice_model: LT1993-4

Required Refdes Prefix: X

spice_lib: LTC3.LIB

Description: 900MHz Low Distortion, Low Noise Differential Amplifier and ADC Driver

Spice_Node_Sequence: [1] VccC , [2] VOVM , [3] VccA , [4] VeeA , [5] +OUT , [6] +OUTFILT , [7] -OUTFILT , [8] -OUT , [9] VeeB , [10] VccB , [11] _EN , [12] VeeC , [13] -INB , [14] -INA , [15] +INB , [16] +INA ,

[LTC6101.asy](#)

spice_model: LTC6101

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: High Voltage, High-Side Current Sense in SOT-23

Spice_Node_Sequence: [1] OUT , [2] VEE , [3] IN- , [4] IN+ , [5] VCC ,

[LTC1992-10.asy](#)

spice_model: LTC1992-10

Required Refdes Prefix: X

spice_lib: LTC4.LIB

Description: Low Power, Fully Differential I/O Amplifier

Spice_Node_Sequence: [1] IN- , [2] VOVM , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] VMID , [8] IN+ ,

[LT1632.asy](#)

spice_model: LT1632

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 45MHz, 45V/ μ s, Rail-to-Rail Input and Output Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1678.asy](#)

spice_model: LT1678

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Low Noise, Rail-to-Rail, Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LTC1152.asy](#)

spice_model: LTC1152

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Rail-to-Rail Input and Output Zero-Drift Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] Out , [6] Comp , [7] SHDN , [8] Cp ,

[LT1218.asy](#)

spice_model: LT1218

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Rail-to-Rail Input and Output Op Amp

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LTC1992-5.asy](#)

spice_model: LTC1992-5

Required Refdes Prefix: X

spice_lib: LTC4.LIB

Description: Low Power, Fully Differential I/O Amplifier

Spice_Node_Sequence: [1] IN- , [2] VOCM , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] VMID , [8] IN+ , [LT1001S8.asy](#)

spice_model: LT1001S8

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [LTC6255.asy](#)

spice_model: LTC6255

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: 6.5MHz, 65 μ A Power Efficient Rail-to-Rail I/O Op Amp

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN , [LT1217.asy](#)

spice_model: LT1217

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Power High Speed Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [LTC1043.asy](#)

spice_model: LTC1043

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Instrumentation Switched Capacitor Building Block

Spice_Node_Sequence: [1] S1A , [2] S2A , [3] Ca+ , [4] S3A , [5] S4A , [6] Ca- , [7] Cosc , [8] Vdd , [9] Vss ,

[LTC1100A.asy](#)

spice_model: LTC1100A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, Chopper-Stabilized Instrumentation Amplifier

Spice_Node_Sequence: [1] GND , [2] CMRR , [3] In- , [4] V- , [5] V+ , [6] In+ , [7] Comp , [8] Out , [LTC1992-2.asy](#)

spice_model: LTC1992-2

Required Refdes Prefix: X

spice_lib: LTC4.LIB

Description: Low Power, Fully Differential I/O Amplifier

Spice_Node_Sequence: [1] IN- , [2] VOCM , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] VMID , [8] IN+ , [LT1213.asy](#)

spice_model: LT1213

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 28MHz, 12V/ μ s, Single Supply Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [LT1351.asy](#)

spice_model: LT1351

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 250 μ A, 3MHz, 200V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN , [LT1882.asy](#)

spice_model: LT1882

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Rail-to-Rail Output pA Input Precision Operational Amplifier(1 of 4)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC6404-2.asy](#)

spice_model: LTC6404-2

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: 600MHz, Low Noise, High Precision Fully Differential Input/Output Amplifier/Driver\n\nNote: Distortion is not modeled.

Spice_Node_Sequence: [1] IN- , [2] Vocm , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] _SHDN , [8] IN+ , [9] OUTF+ , [10] OUTF- ,

[UniversalOpamp2.asy](#)

spice_lib: level.2

Required Refdes Prefix: X

Description: Universal Opamp model that allows 4 different levels of simulation accuracy. See ./examples/Educational/UniversalOpamp2.asc for details. En and in are equivalent voltage and current noises. Enk and ink are the respective corner frequencies. Phimargin is used to set the 2nd pole or delay to the approximate phase margin for level.3a and level.3b. This version uses the new, experimental level 2 switch as the output devices.

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1216.asy](#)

spice_model: LT1216

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 23MHz, 50V/ μ s, Single Supply Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1457.asy](#)

spice_model: LT1457

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual Precision JFET Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT6200-5.asy](#)

spice_model: LT6200-5

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 800MHz, Av=5 Rail-to-Rail Low Noise Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] SHDN_L ,

[LT1006A.asy](#)

spice_model: LT1006A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Single Supply Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1398.asy](#)

spice_model: LT1398

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Low Cost 300MHz Current Feedback Amplifier with Shutdown

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1037A.asy](#)

spice_model: LT1037A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LTC7652.asy](#)

spice_model: LTC7652

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Chopper-Stabilized Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1354.asy](#)

spice_model: LT1354

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 12MHz, 400V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1211.asy](#)

spice_model: LT1211

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 14MHz, 7V/ μ s, Single Supply Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1787.asy](#)

spice_model: LT1787

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Side Current Sense Amplifier

Spice_Node_Sequence: [1] FIL- , [2] Vs- , [3] VEE , [4] Vout , [5] Vbias , [6] Vs+ , [7] FIL+ ,
[LT1057A.asy](#)

spice_model: LT1057A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual JFET Input Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT6107.asy](#)

spice_model: LT6107

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: High Voltage, High-Side Current Sense in SOT-23

Spice_Node_Sequence: [1] OUT , [2] VEE , [3] IN- , [4] IN+ , [5] VCC ,
[LT6201.asy](#)

spice_model: LT6201

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 165 MHz, Rail-to-Rail, 0.95 nV/rtHz Low Noise Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT6551.asy](#)

spice_model: LT6551

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 3.3V Quad Video Amplifier

Spice_Node_Sequence: [1] IN , [2] GND , [3] VCC , [4] VEE , [5] OUT ,

[LT1462.asy](#)

spice_model: LT1462

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, 260 μ W C-Load Picoampere Bias Current JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT2179.asy](#)

spice_model: LT2179

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 17mA Max, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1357.asy](#)

spice_model: LT1357

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 25MHz, 600V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC1542.asy](#)

spice_model: LTC1542

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power Operational Amplifier and Comparator

Spice_Node_Sequence: [1] AMPOUT , [2] AMPIN- , [3] AMPIN+ , [4] Vss , [5] COMPIN+ , [6] COMPIN- , [7] COMPOUT , [8] Vcc ,

[LT6558.asy](#)

spice_model: LT6558

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 550MHz, 2200V/ μ s Gain of 1, Single Supply Triple Video Amplifier with Input Bias Control

Spice_Node_Sequence: [1] _EN , [2] GND , [3] INR , [4] AGND1 , [5] ING , [6] GNDG , [7] INB , [8] GNDB , [9] V+B , [10] OUTB , [11] V+G , [12] OUTG , [13] V+R , [14] OUTR , [15] V+ , [16] BCV ,

[LT1328PD.asy](#)

spice_model: LT1328PD

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: A photodiode similar to a SFH-205 for testing the LT1328, drive the light input with a current source

Spice_Node_Sequence: [1] A , [2] C , [3] L ,

[LT1631.asy](#)

spice_model: LT1631

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 30MHz, 10V/ μ s, Rail-to-Rail Input and Output Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC1541.asy](#)

spice_model: LTC1541

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power Operational Amplifier, Comparator and Reference

Spice_Node_Sequence: [1] AMPOUT , [2] AMPIN- , [3] AMPIN+ , [4] Vss , [5] COMPIN+ , [6] Ref , [7] COMPOUT , [8] Vcc ,

[LTC6084.asy](#)

spice_model: LTC6084

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: Dual 1.5MHz, Rail-to-Rail, CMOS Operational Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1366.asy](#)

spice_model: LT1366

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC1053.asy](#)

spice_model: LTC1053

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Precision Zero-Drift Operational Amplifier with Internal Capacitors

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1208.asy](#)

spice_model: LT1208

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 45MHz, 400V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1229.asy](#)

spice_model: LT1229

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 100MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1990.asy](#)

spice_model: LT1990

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: \pm 250V Input Range G=1,10 μ Power Difference Amplifier

Spice_Node_Sequence: [1] REF , [2] IN- , [3] IN+ , [4] VEE , [5] GAIN2 , [6] OUT , [7] VCC , [8] GAIN1 ,

[LTC6085.asy](#)

spice_model: LTC6085

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: Quad 1.5MHz, Rail-to-Rail, CMOS Operational Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6557.asy](#)

spice_model: LT6557

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 500MHz, 2200V/ μ s Gain of 2, Single Supply Triple Video Amplifier with Input Bias Control

Spice_Node_Sequence: [1] _EN , [2] GND , [3] INR , [4] AGND1 , [5] ING , [6] GNDG , [7] INB , [8] GNDB , [9] V+B , [10] OUTB , [11] V+G , [12] OUTG , [13] V+R , [14] OUTR , [15] V+ , [16] BCV ,

[LT2178A.asy](#)

spice_model: LT2178A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 17mA Max, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1783.asy](#)

spice_model: LT1783

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 1.2MHz μ Power Over-The-Top, SOT-23, Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1058.asy](#)

spice_model: LT1058

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad JFET Input Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1210.asy](#)

spice_model: LT1210

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 1.1A, 35MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] CMP ,

[LT6109-2.asy](#)

spice_model: LT6109-2

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: High Side Current Sense Amplifier with Reference and Comparators

Spice_Node_Sequence: [1] SenseLo , [2] EN/_RST , [3] OutC2 , [4] OutC1 , [5] V- , [6] INC1 , [7] INC2 , [8] OutA , [9] V+ , [10] SenseHi ,

[LT6232.asy](#)

spice_model: LT6232

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 215 MHz, Rail-to-Rail Output, 1.1nV/rtHz, 3.4mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1672.asy](#)

spice_model: LT1672

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Single 2 μ A, $A_v > 5$ Over-The-Top Precision Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LTC6079.asy](#)

spice_model: LTC6079

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: Quad μ Power Precision, CMOS Rail-to-Rail Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT2079.asy](#)

spice_model: LT2079

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1187.asy](#)

spice_model: LT1187

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Power Video Difference Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] REF , [7] FB ,
[LT1022A.asy](#)

spice_model: LT1022A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: High Speed, Precision JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1789-10.asy](#)

spice_model: LT1789-10

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: μ Power, Single Supply Rail-to-Rail Output Instrumentation Amplifier

Spice_Node_Sequence: [1] RG1 , [2] IN- , [3] IN+ , [4] VEE , [5] REF , [6] OUT , [7] VCC , [8] RG2 ,
[LT1819.asy](#)

spice_model: LT1819

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 400MHz, 2500V/ μ s, 9mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,
[LT1995.asy](#)

spice_model: LT1995

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: 30MHz,1000V/use Gain Selectable Amplifier

Spice_Node_Sequence: [1] P1 , [2] P2 , [3] P4 , [4] Vee , [5] Ref , [6] OUT , [7] VCC , [8] M4 , [9] M2 ,
[10] M1 ,
[LTC6404-1.asy](#)

spice_model: LTC6404-1

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: 600MHz, Low Noise, High Precision Fully Differential Input/Output
Amplifier/Driver\n\nNote: Distortion is not modeled.

Spice_Node_Sequence: [1] IN- , [2] Vocm , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] _SHDN , [8] IN+ ,
[9] OUTF+ , [10] OUTF- ,
[LT1028A.asy](#)

spice_model: LT1028A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra Low Noise Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1886.asy](#)

spice_model: LT1886

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 700MHz, $A_v \geq 10$, 200mA Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LTC6244.asy](#)

spice_model: LTC6244

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 50MHz Low Noise, Rail-to-Rail, CMOS Op Amp
Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6402-20.asy](#)

spice_model: LT6402-20

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 300MHz Low Distortion, Low Noise Differential Amplifier/ADC Driver

Spice_Node_Sequence: [1] VccC , [2] VOVM , [3] VccA , [4] VeeA , [5] +OUT , [6] +OUTFILT , [7] -OUTFILT , [8] -OUT , [9] VeeB , [10] VccB , [11] _EN , [12] VeeC , [13] -INB , [14] -INA , [15] +INB , [16] +INA ,

[LT1466L.asy](#)

spice_model: LT1466L

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1214.asy](#)

spice_model: LT1214

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 28MHz, 12V/ μ s, Single Supply Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6010.asy](#)

spice_model: LT6010

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 135 μ A, 14nV/rtHz, Rail-to-Rail Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT , [6] SHDN ,

[LT1178A.asy](#)

spice_model: LT1178A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 17mA Max, Dual Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1230.asy](#)

spice_model: LT1230

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 100MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1881.asy](#)

spice_model: LT1881

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Rail-to-Rail Output pA Input Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC6401-20.asy](#)

spice_model: LTC6401-20

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: 1.3GHz Low Noise, Low Distortion Differential ADC Driver for DC-140MHz IF\n\nNote: Distortion is not modeled.

Spice_Node_Sequence: [1] IN- , [2] Vocm , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] _EN , [8] IN+ , [9]

OUTF- , [10] OUTF+ ,

[LT1396.asy](#)

spice_model: LT1396

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 400MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC1151.asy](#)

spice_model: LTC1151

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual +-15V Zero-Drift Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6350.asy](#)

spice_model: LT6350

Required Refdes Prefix: X

Description: Single-Ended to Differential Converter/ADC Driver

spice_lib: LTC7.lib

Spice_Node_Sequence: [1] -IN1 , [2] +IN2 , [3] V+ , [4] OUT1 , [5] OUT2 , [6] V- , [7] _SHDN , [8] +IN1 ,

[LT1722.asy](#)

spice_model: LT1722

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 200MHz Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC6253.asy](#)

spice_model: LTC6253

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: Dual, 720MHz, 3.5mA, R-R I/O Op Amp

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 ,

[LTC6910-2.asy](#)

spice_model: LTC6910-2

Required Refdes Prefix: X

spice_lib: LTC1.LIB

Description: Digitally Controlled Programmable Gain Amplifiers in SOT-23

Spice_Node_Sequence: [1] OUT , [2] AGND , [3] IN , [4] V- , [5] G0 , [6] G1 , [7] G2 , [8] V+ ,

[LT1013A.asy](#)

spice_model: LT1013A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1493.asy](#)

spice_model: LT1493

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 5MHz, 3V/ μ s Low Power Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1077A.asy](#)

spice_model: LT1077A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1013D.asy](#)

spice_model: LT1013D

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LTC6910-1.asy](#)

spice_model: LTC6910-1

Required Refdes Prefix: X

spice_lib: LTC1.LIB

Description: Digitally Controlled Programmable Gain Amplifiers in SOT-23

Spice_Node_Sequence: [1] OUT , [2] AGND , [3] IN , [4] V- , [5] G0 , [6] G1 , [7] G2 , [8] V+ ,
[LT6204.asy](#)

spice_model: LT6204

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 100 MHz, Rail-to-Rail, 1.9nV/rtHz Low Power Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,
[LT1360.asy](#)

spice_model: LT1360

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 50MHz, 800V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT6559.asy](#)

spice_model: LT6559

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: Low Cost 5V/ \pm 5V 300MHz Triple Video Amplifier in QFN

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT1101.asy](#)

spice_model: LT1101

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, μ Power, Single Supply Instrumentation Amplifier(Fixed Gain = 10 or 100)

Spice_Node_Sequence: [1] GND , [2] Tap1 , [3] In- , [4] V- , [5] V+ , [6] In+ , [7] Tap2 , [8] Out ,
[LT2078.asy](#)

spice_model: LT2078

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT6234.asy](#)

spice_model: LT6234

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 60MHz, Rail-to-Rail Output, 1.9nV/rtHz, 1.2mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT1114.asy](#)

spice_model: LT1114

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Low Power Precision, Picoamp Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6003.asy](#)

spice_model: LT6003

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 1.6V, 1 μ A Precision Rail-to-Rail Input and Output Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1999-50.asy](#)

spice_model: LT1999-50

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: High Voltage, Bidirectional Current Sense Amplifier

Spice_Node_Sequence: [1] V+ , [2] +IN , [3] -IN , [4] GND , [5] Ref , [6] Vout , [7] _SHDN ,

[LT6411.asy](#)

spice_model: LT6411

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: 650MHz Differential ADC Driver/Dual Selectable Gain Amplifier

Spice_Node_Sequence: [1] Vee , [2] OUT2 , [3] Vcc , [4] OUT1 , [5] _EN , [6] DGND , [7] IN1+ , [8] IN1- , [9] IN2- , [10] IN2+ ,

[LT1674.asy](#)

spice_model: LT1674

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 2 μ A, $A_v > 5$ Over-The-Top Precision Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1055.asy](#)

spice_model: LT1055

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1630.asy](#)

spice_model: LT1630

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 30MHz, 10V/ μ s, Rail-to-Rail Input and Output Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1677.asy](#)

spice_model: LT1677

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, Rail-to-Rail Input and Output Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1224.asy](#)

spice_model: LT1224

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Very High Speed Operational Amplifier($A_v \geq 1$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1469-2.asy](#)

spice_model: LT1469-2

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: Dual 90MHz, 22V/ μ s, 16-Bit Accurate Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6211.asy](#)

spice_model: LT6211

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: Dual Programmable Supply Current, R-R Output, Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] V-1 ,

[LT1818.asy](#)

spice_model: LT1818

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 400MHz, 2500V/ μ s, 9mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1058A.asy](#)

spice_model: LT1058A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad JFET Input Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1802.asy](#)

spice_model: LT1802

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 80MHz, 25V/ μ s Low Power Rail-to-Rail Input and Output Precision Op Amp(1 of 4)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6109-1.asy](#)

spice_model: LT6109-1

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: High Side Current Sense Amplifier with Reference and Comparators

Spice_Node_Sequence: [1] SenseLo , [2] EN/_RST , [3] OutC2 , [4] OutC1 , [5] V- , [6] INC1 , [7] INC2 , [8] OutA , [9] V+ , [10] SenseHi ,

[LT6552.asy](#)

spice_model: LT6552

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 3.3V Single Supply Video Difference Amplifier

Spice_Node_Sequence: [1] Ref , [2] IN- , [3] IN+ , [4] VEE , [5] SHDN_L , [6] OUT , [7] VCC , [8] FB ,

[LT1993-2.asy](#)

spice_model: LT1993-2

Required Refdes Prefix: X

spice_lib: LTC3.LIB

Description: 800MHz Low Distortion, Low Noise Differential Amplifier and ADC Driver

Spice_Node_Sequence: [1] VccC , [2] VOVM , [3] VccA , [4] VeeA , [5] +OUT , [6] +OUTFILT , [7] -OUTFILT , [8] -OUT , [9] VeeB , [10] VccB , [11] _EN , [12] VeeC , [13] -INB , [14] -INA , [15] +INB , [16] +INA ,

[LT6004.asy](#)

spice_model: LT6004

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: Dual 1.6V, 1 μ A Precision Rail-to-Rail Input and Output Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1022.asy](#)

spice_model: LT1022

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: High Speed, Precision JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1008.asy](#)

spice_model: LT1008

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Picoamp Input Current, μ Volt Offset, Low Noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] COMP1 , [7] COMP2 ,

[LT6210.asy](#)

spice_model: LT6210

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: Programmable Supply Current, R-R Output, Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] V-1 ,

[LT1970.asy](#)

spice_model: LT1970

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: 500mA Power Op Amp with Adjustable Precision Current Limit

Spice_Node_Sequence: [1] Vee , [2] V- , [3] OUT , [4] Sense+ , [5] Filter , [6] Sense- , [7] Vcc , [8] -IN , [9] +IN , [10] VCSnk , [11] VCsrc , [12] COM , [13] Enable , [14] _Isrc , [15] _Isnk , [16] V+ ,

[LT1369.asy](#)

spice_model: LT1369

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1363.asy](#)

spice_model: LT1363

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 70MHz, 1000V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1994.asy](#)

spice_model: LT1994

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Low Noise, Low Distortion Fully Differential I/O Amplifier/Driver

Spice_Node_Sequence: [1] IN- , [2] VOCM , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] SHDN , [8] IN+ ,

[LT1012S8.asy](#)

spice_model: LT1012S8

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Picoamp Input Current, μ Volt Offset, Low noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1057S.asy](#)

spice_model: LT1057S

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual JFET Input Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1431.asy](#)

spice_model: LT1431

Required Refdes Prefix: X

spice_lib: LT1431.sub

Description: Programmable Reference

Spice_Node_Sequence: [1] Coll , [2] Comp , [3] V+ , [4] Rtop , [5] GND-S , [6] GND-F , [7] Rmid , [8] Ref ,

[LT1679.asy](#)

spice_model: LT1679

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Low Noise, Rail-to-Rail, Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1355.asy](#)

spice_model: LT1355

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 12MHz, 400V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6205.asy](#)

spice_model: LT6205

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Single Supply 3V, 100MHz Video Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1028.asy](#)

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra-Low Noise Precision High Speed Operational Amplifier

spice_model: LT1028

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1012D.asy](#)

spice_model: LT1012D

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Picoamp Input Current, μ Volt Offset, Low noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1124.asy](#)

spice_model: LT1124

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Operational Amplifier Dual Low Noise, High Speed Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1195.asy](#)

spice_model: LT1195

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Power, Ultra High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1793.asy](#)

spice_model: LT1793

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, Picoampere Bias Current, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC6401-26.asy](#)

spice_model: LTC6401-26

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: 1.3GHz Low Noise, Low Distortion Differential ADC Driver for DC-140MHz IF\n\nNote: Distortion is not modeled.

Spice_Node_Sequence: [1] IN- , [2] Vocm , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] _EN , [8] IN+ , [9] OUTF- , [10] OUTF+ ,

[LT1809.asy](#)

spice_model: LT1809

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Single 180MHz, 350V/ μ s, Rail-to-Rail Input and Output, Low Distortion, Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1228.asy](#)

spice_model: LT1228

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 100MHz Current Feedback Amplifier with DC Gain Control

Spice_Node_Sequence: [1] Iout , [2] In- , [3] In+ , [4] V- , [5] Iset , [6] Vout , [7] V+ , [8] Gain ,

[LT1002.asy](#)

spice_model: LT1002

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1013.asy](#)

spice_model: LT1013

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1055S8.asy](#)

spice_model: LT1055S8

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1880.asy](#)

spice_model: LT1880

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Rail-to-Rail Output pA Input Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1353.asy](#)

spice_model: LT1353

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 250mA, 3MHz, 200V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1194.asy](#)

spice_model: LT1194

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Video Difference Amplifier, Gain of 10

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] REF , [7] VOS1 , [8] VOS2 ,

[LT6108-2.asy](#)

spice_model: LT6108-2

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: High Side Current Sense Amplifier with Reference and Comparators

Spice_Node_Sequence: [1] SenseLo , [2] EN/_RST , [3] OutC , [4] V- , [5] INC , [6] OutA , [7] V+ , [8] SenseHi ,

[LT1012A.asy](#)

spice_model: LT1012A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Picoamp Input Current, μ Volt Offset, Low noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1055A.asy](#)

spice_model: LT1055A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1079A.asy](#)

spice_model: LT1079A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Quad, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1397.asy](#)

spice_model: LT1397

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 400MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[opamp.asy](#)

Required Refdes Prefix: X

Description: Ideal single-pole operational amplifier. You must .lib opamp.sub

spice_model: opamp

Spice_Node_Sequence: [1] invin , [2] noninvin , [3] out ,

[LT1813.asy](#)

spice_model: LT1813

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 3mA, 100MHz, 750V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1115.asy](#)

spice_model: LT1115

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra-Low Noise, Low Distortion, Audio Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6011.asy](#)

spice_model: LT6011

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 135 μ A, 14nV/rtHz, Rail-to-Rail Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT6200.asy](#)

spice_model: LT6200

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 165 MHz, Rail-to-Rail Input and Output, 0.95nV/rtHz Low Noise Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] SHDN_L ,

[LTC1051.asy](#)

spice_model: LTC1051

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Zero-Drift Operational Amplifier with Internal Capacitors

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1468-2.asy](#)

spice_model: LT1468-2

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: 90MHz, 22V/ μ s, 16-Bit Accurate Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6203.asy](#)

spice_model: LT6203

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 100 MHz, Rail-to-Rail, 1.9nV/rtHz Low Power Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1259.asy](#)

spice_model: LT1259

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Cost Dual 130MHz Current Feedback Amplifier with Shutdown

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1192.asy](#)

spice_model: LT1192

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra High Speed Operational Amplifier($A_v \geq 5$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1024.asy](#)

spice_model: LT1024

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual, Matched Picoamp μ Volt Input, Low Noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1495.asy](#)

spice_model: LT1495

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 1.5 μ A Max Over-the-Top Precision Rail-to-Rail Input and Output Opamp(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6220.asy](#)

spice_model: LT6220

Required Refdes Prefix: X

spice_lib: LTC4.LIB

Description: 60MHz, 20V/us, Low Power, RRIO Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1991.asy](#)

spice_model: LT1991

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Precision, 100 μ A Gain Selectable Amplifier

Spice_Node_Sequence: [1] P1 , [2] P3 , [3] P9 , [4] Vee , [5] Ref , [6] OUT , [7] VCC , [8] M9 , [9] M3 , [10] M1 ,

[LT1102A.asy](#)

spice_model: LT1102A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: High Speed, Precision, JFET Input Instrumentation Amplifier(Fixed Gain = 10 or 100)

Spice_Node_Sequence: [1] GND , [2]

[LT6600-2.5.asy](#)

spice_model: LT6600-2.5

Required Refdes Prefix: X

spice_lib: LT6600-2.5.sub

Description: Very Low Noise, Differential Amplifier and 2.5MHz Lowpass Filter

Spice_Node_Sequence: [1] IM , [2] VOVM , [3] VCC , [4] OP , [5] OM , [6] VEE , [7] VMID , [8] IP ,

[LT6600-20.asy](#)

spice_model: LT6600-20

Required Refdes Prefix: X

spice_lib: LT6600-20.sub

Description: Very Low Noise, Differential Amplifier and 20MHz Lowpass Filter

Spice_Node_Sequence: [1] IM , [2] VOVM , [3] VCC , [4] OP , [5] OM , [6] VEE , [7] VMID , [8] IP ,

[LT6600-10.asy](#)

spice_model: LT6600-10

Required Refdes Prefix: X

spice_lib: LT6600-10.sub

Description: Very Low Noise, Differential Amplifier and 10MHz Lowpass Filter

Spice_Node_Sequence: [1] IM , [2] VOVM , [3] VCC , [4] OP , [5] OM , [6] VEE , [7] VMID , [8] IP ,

[LT1568.asy](#)

spice_model: LT1568

Required Refdes Prefix: X

spice_lib: LT1568.sub

Description: Very Low Noise, High Frequency Active RC, Filter Building Block

Spice_Node_Sequence: [1] V+ , [2] INVA , [3] SA , [4] OUTA , [5] _OUTA , [6] GNDA , [7] NC , [8] V- , [9] V- , [10] _EN , [11] GNDB , [12] _OUTB , [13] OUTB , [14] SB , [15] INVB , [16] V+ ,

[LTC1565.asy](#)

spice_model: LTC1565

Description: Linear Phase, High Frequency Lowpass Filter

Required Refdes Prefix: X

Spice_Node_Sequence: [1] +IN , [2] -IN , [3] GND , [4] V- , [5] _SHDN , [6] V+ , [7] -OUT , [8] +OUT ,

[LTC1562-2.asy](#)

spice_model: LTC1562-2

Required Refdes Prefix: X

spice_lib: LTC1562-2.sub

Description: Very Low Noise, Low Distortion Active RC Quad Universal Filter NOTE: GBW, slew rate and noise are modeled in this otherwise highly idealized macromodel.

Spice_Node_Sequence: [1] INV B , [2] V1 B , [3] V2 B , [4] V+ , [5] V2 A , [6] V1 A , [7] INV A , [8] INV D , [9] V1 D , [10] V2 D , [11] AGND , [12] V- , [13] V2 C , [14] V1 C , [15] INV C ,

[LTC1562.asy](#)

spice_model: LTC1562

Required Refdes Prefix: X

spice_lib: LTC1562.sub

Description: Very Low Noise, Low Distortion Active RC Quad Universal Filter NOTE: GBW, slew rate and noise are modeled in this otherwise highly idealized macromodel.

Spice_Node_Sequence: [1] INV B , [2] V1 B , [3] V2 B , [4] V+ , [5] V2 A , [6] V1 A , [7] INV A , [8] INV D , [9] V1 D , [10] V2 D , [11] AGND , [12] V- , [13] V2 C , [14] V1 C , [15] INV C ,

[LTC1563-2.asy](#)

spice_model: LTC1563-2

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Active RC, 4th Order Lowpass Filter

Spice_Node_Sequence: [1] _LP , [2] SA , [3] INVA , [4] LPA , [5] AGND , [6] V- , [7] _EN , [8] SB , [9] INVB , [10] LPB , [11] V+ ,

[LTC1563-3.asy](#)

spice_model: LTC1563-3

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Active RC, 4th Order Lowpass Filter

Spice_Node_Sequence: [1] _LP , [2] SA , [3] INVA , [4] LPA , [5] AGND , [6] V- , [7] _EN , [8] SB , [9] INVB , [10] LPB , [11] V+ ,

[LTC1562A.asy](#)

spice_model: LTC1562A

Required Refdes Prefix: X

spice_lib: LTC1562.sub

Description: Very Low Noise, Low Distortion Active RC Quad Universal Filter NOTE: GBW, slew rate and noise are modeled in this otherwise highly idealized macromodel.

Spice_Node_Sequence: [1] INV B , [2] V1 B , [3] V2 B , [4] V+ , [5] V2 A , [6] V1 A , [7] INV A , [8] INV D , [9] V1 D , [10] V2 D , [11] AGND , [12] V- , [13] V2 C , [14] V1 C , [15] INV C ,

[2N7002.asy](#)

spice_model: 2N7002

Required Refdes Prefix: X

spice_lib: 2N7002.mod

Description: Small signal N-Channel MOSFET transistor

Spice_Node_Sequence: [1] D , [2] G , [3] S ,
[neonbulb.asy](#)

Required Refdes Prefix: X

Description: Parameterized Neon Bulb

spice_lib: neonbulb

Spice_Node_Sequence: [1] V+ , [2] V- ,
[urc.asy](#)

spice_model: URC

Required Refdes Prefix: U

Description: Uniform RC-line. Intended for interconnection on IC's but rarely used.

Spice_Node_Sequence: [1] A , [2] B , [3] C ,
[DIP10.asy](#)

Required Refdes Prefix: X

Description: Generic Symbol for use with subcircuits that you supply.

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 , [7] 7 , [8] 8 , [9] 9 , [10] 10 ,
[battery.asy](#)

spice_model: V

Required Refdes Prefix: V

Description: Voltage Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,
[DIAC.asy](#)

spice_model: DIAC

Required Refdes Prefix: X

Description: Generic Bi-directional Trigger Device symbol for use with a model that you supply

Spice_Node_Sequence: [1] + , [2] - ,
[Epoly.asy](#)

spice_model: POLY()

Required Refdes Prefix: E

Description: Voltage dependent voltage source with two terminals. Useful for drafting schematic generating archaic voltage controlled syntax.

Spice_Node_Sequence: [1] + , [2] - ,
[DIP16.asy](#)

Required Refdes Prefix: X

Description: Generic Symbol for use with subcircuits that you supply.

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 , [7] 7 , [8] 8 , [9] 9 , [10] 10 , [11] 11 , [12] 12 , [13] 13 , [14] 14 , [15] 15 , [16] 16 ,

[EuropeanResistor.asy](#)

spice_model: R

Required Refdes Prefix: R

Description: A Resistor(European style graphic)

Spice_Node_Sequence: [1] A , [2] B ,
[EuropeanCap.asy](#)

spice_model: C

Required Refdes Prefix: C

Description: Polarized Capacitor

Spice_Node_Sequence: [1] A , [2] B ,
[DIP14.asy](#)

Required Refdes Prefix: X

Description: Generic Symbol for use with subcircuits that you supply.

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 , [7] 7 , [8] 8 , [9] 9 , [10] 10 , [11] 11 , [12] 12 , [13] 13 , [14] 14 ,

[Gpoly.asy](#)

spice_model: POLY()

Required Refdes Prefix: G

Description: Voltage dependent current source with two terminals. Useful for drafting schematic generating archaic voltage controlled syntax.

Spice_Node_Sequence: [1] + , [2] - ,

[pigbt.asy](#)

spice_model: PIGBT

Required Refdes Prefix: X

Description: P-Channel IBGT symbol for a model you supply.

Spice_Node_Sequence: [1] C , [2] G , [3] E ,

[triode.asy](#)

spice_model: Triode

Required Refdes Prefix: X

Description: This symbol is for use with a subcircuit macromodel that you supply.

Spice_Node_Sequence: [1] Plate , [2] Grid , [3] Cathode ,

[xtal.asy](#)

spice_model: C

Required Refdes Prefix: C

Description: Piezoelectric crystal. Set C, Lser and Cpar to set series and parallel resonances. This is actually the same as a capacitor.

Spice_Node_Sequence: [1] A , [2] B ,

[DIP20.asy](#)

Required Refdes Prefix: X

Description: Generic Symbol for use with subcircuits that you supply.

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 , [7] 7 , [8] 8 , [9] 9 , [10] 10 , [11] 11 , [12] 12 , [13] 13 , [14] 14 , [15] 15 , [16] 16 , [17] 17 , [18] 18 , [19] 19 , [20] 20 ,

[SCR.asy](#)

spice_model: SCR

Required Refdes Prefix: X

Description: Generic SCR symbol for use with a model that you supply.

Spice_Node_Sequence: [1] A , [2] G , [3] K ,

[pentode.asy](#)

spice_model: Pentrode

Required Refdes Prefix: X

Description: This symbol is for use with a subcircuit macromodel that you supply.

Spice_Node_Sequence: [1] Cathode , [2] G1 , [3] G2 , [4] G3 , [5] Anode ,

[urc2.asy](#)

spice_model: URC

Required Refdes Prefix: U

Description: Uniform RC-line. Intended for interconnection on IC's but rarely used.

Spice_Node_Sequence: [1] A , [2] B , [3] C ,

[TowTom2.asy](#)

spice_model: TowTom2

Required Refdes Prefix: X

Description: 2nd Order Tow-Thomas Filter building block. Needs .lib TowTom2.sub & .params R=10K C=160p GBW1=10Meg GBW2=15Meg Aol=100K

Spice_Node_Sequence: [1] V1 , [2] V2 , [3] INV ,

[cell.asy](#)

spice_model: V

Required Refdes Prefix: V

Description: Voltage Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,

[tetrode.asy](#)

spice_model: Tetrode

Required Refdes Prefix: X

Description: This symbol is for use with a subcircuit macromodel that you supply.

Spice_Node_Sequence: [1] Anode , [2] Screen , [3] Grid , [4] Cathode ,
[DIP8.asy](#)

Required Refdes Prefix: X

Description: Generic Symbol for use with subcircuits that you supply.

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 , [7] 7 , [8] 8 ,
[nigbt.asy](#)

spice_model: NIGBT

Required Refdes Prefix: X

Description: N-Channel IGBT symbol for a model you supply.

Spice_Node_Sequence: [1] C , [2] G , [3] E ,
[xvaristor.asy](#)

spice_model: varistor

Required Refdes Prefix: X

Description: Generic varistor symbol for use with a model that you supply.

Spice_Node_Sequence: [1] A , [2] B ,
[jumper.asy](#)

Required Refdes Prefix: J

Description: A wire jumper. This component lets you give the same net two different names

Spice_Node_Sequence: [1] + , [2] - ,
[EuropeanPolcap.asy](#)

spice_model: C

Required Refdes Prefix: C

Description: Polarized Capacitor

Spice_Node_Sequence: [1] A , [2] B ,
[TRIAC.asy](#)

spice_model: TRIAC

Required Refdes Prefix: X

Description: Generic TRIAC symbol for use with a model that you supply.

Spice_Node_Sequence: [1] MT2 , [2] G , [3] MT1 ,
[fixedind.asy](#)

spice_model: L

Required Refdes Prefix: L

Description: An inductor that cannot be edited. Typically used with the 1533/1534

Spice_Node_Sequence: [1] A , [2] B ,
[signal.asy](#)

spice_model: SINE(0 1 1K)

Required Refdes Prefix: V

Description: Voltage Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,
[NE555.asy](#)

spice_model: NE555

Required Refdes Prefix: X

spice_lib: NE555.sub

Description: An idealized 555 timer model.

Spice_Node_Sequence: [1] GND , [2] TRIG , [3] OUT , [4] RST , [5] CV , [6] THRS , [7] DIS , [8] Vcc ,

[LT1021-10.asy](#)

spice_model: LT1021-10

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Precision 10V Reference

Spice_Node_Sequence: [1] IN , [2] GND , [3] OUT , [4] TRIM ,
[LT1021-5.asy](#)

spice_model: LT1021-5

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Precision 5V Reference

Spice_Node_Sequence: [1] IN , [2] GND , [3] OUT , [4] TRIM ,
[LT1021-7.asy](#)

spice_model: LT1021-7

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Precision 7V Reference

Spice_Node_Sequence: [1] IN , [2] GND , [3] OUT ,
[LT1009.asy](#)

spice_model: LT1009

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: 2.5V Adjustable Reference

Spice_Node_Sequence: [1] VCC , [2] VEE , [3] ADJ ,

[LT1764A-2.5.asy](#)

spice_model: LT1764A-2.5

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Fixed 2.5V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,
[LT3517.asy](#)

spice_model: LT3517

Required Refdes Prefix: X

spice_lib: LT3517.sub

Description: Full-Featured LED Controller with 1.5A Switch Current

Spice_Node_Sequence: [1] Vin , [2] _SHDN , [3] Vref , [4] Rt , [5] SYNC , [6] SS , [7] PWM , [8] CTRL ,
[9] Vc , [10] FB , [11] TGEN , [12] ISN , [13] ISP , [14] TG , [15] SW , [16] GND ,

[LT3781.asy](#)

spice_model: LT3781

Required Refdes Prefix: X

spice_lib: LT3781.sub

Description: "Bootstrap" Start Dual Transistor Synchronous Forward Controller

Spice_Node_Sequence: [1] _SHDN , [2] OVLO , [3] THERM , [4] GSND , [5] 5Vref , [6] FSET , [7] SS ,
[8] FB , [9] Vc , [10] Sense , [11] SG , [12] Vcc , [13] PGND , [14] GB , [15] BSTREF , [16] TG , [17]
Vbst ,

[LTC3526B-2.asy](#)

spice_model: LTC3526B-2

Required Refdes Prefix: X

spice_lib: LTC3526B-2.sub

Description: 500mA 2MHz Synchronous Step-up DC/DC Converter in 2mm*2mm DFN

Spice_Node_Sequence: [1] SW , [2] GND , [3] Vin , [4] _SHDN , [5] FB , [6] Vout ,
[LT1374HV-5.asy](#)

spice_model: LT1374HV-5

Required Refdes Prefix: X

spice_lib: LT1374.sub

Description: 4.5A, 500KHz Step-Down Switching Regulator, Fixed 5Volt Output

Spice_Node_Sequence: [1] Vin , [2] Boost , [3] Sense , [4] GND , [5] BIAS , [6] Vc , [7] _SHDN , [8] SW ,

[LTC3530.asy](#)

spice_model: LTC3530

Required Refdes Prefix: X

spice_lib: LTC3530.sub

Description: Wide Input Voltage Synchronous Buck-Boost DC/DC Converter

Spice_Node_Sequence: [1] Rt , [2] Burst , [3] SW1 , [4] SW2 , [5] GND , [6] Vout , [7] Vin , [8] _SHDN/SS , [9] FB , [10] Vc ,

[LT3757.asy](#)

spice_model: LT3757

Required Refdes Prefix: X

spice_lib: LT3757.sub

Description: Boost, Flyback, SEPIC and Inverting Controller

Spice_Node_Sequence: [1] Vc , [2] FBX , [3] SS , [4] Rt , [5] Sense , [6] Gate , [7] INTVcc , [8] _SHDN/UVLO , [9] Vin , [10] GND ,

[LT3484-1.asy](#)

spice_model: LT3484-1

Required Refdes Prefix: X

spice_lib: LT3484-1.sub

Description: Photoflash Capacitor Charger

Spice_Node_Sequence: [1] _Done , [2] Charge , [3] Vin , [4] SW , [5] Vbat , [6] GND ,

[LTC1872B.asy](#)

spice_model: LTC1872B

Required Refdes Prefix: X

spice_lib: LTC1872B.sub

Description: Constant Frequency Current Mode Step-Up DC/DC Controller in SOT-23

Spice_Node_Sequence: [1] Ith/Run , [2] GND , [3] FB , [4] Sense- , [5] Vin , [6] Ngate ,

[LTC1438.asy](#)

spice_model: LTC1438

Required Refdes Prefix: X

spice_lib: LTC1438.sub

Description: Dual High Efficiency, Low Noise, Synchronous Step-Down Switching Regulator

Spice_Node_Sequence: [1] Run/SS1 , [2] Sense+1 , [3] Sense-1 , [4] Vprog1 , [5] Ith1 , [6] Cosc , [7] SGND , [8] SFB1 , [9] Ith2 , [10] Vosense2 , [11] Sense-2 , [12] Sense+2 , [13] Run/SS2 , [14] AUXDR , [15] AUXFB , [16] AUXON , [17] Boost2 , [18] TGL2 , [19] SW2 , [20] EXTVcc , [21] BG2 , [22] PGND , [23] INTVcc , [24] BG1 , [25] Vin , [26] SW1 , [27] TGL1 , [28] Boost1 ,

[LT3991.asy](#)

spice_model: LT3991

Required Refdes Prefix: X

spice_lib: LT3991.sub

Description: 55V, 1.2A, Step-Down Regulator with 2.8 μ A Quiescent Current\n\nNote: Sync pin is not modeled; Burst Mode is selected.

Spice_Node_Sequence: [1] BD , [2] Boost , [3] SW , [4] Vin , [5] EN , [6] FB , [7] SS , [8] Rt , [9] PG , [10] GND ,

[LT3461.asy](#)

spice_model: LT3461

Required Refdes Prefix: X

spice_lib: LT3461.sub

Description: 1.3MHz Step-Up Converter in ThinSOT

Spice_Node_Sequence: [1] SW , [2] GND , [3] FB , [4] _SHDN , [5] Vin , [6] Vout ,
[LT1610.asy](#)

spice_model: LT1610

Required Refdes Prefix: X

spice_lib: LT1610.sub

Description: 1.7MHz, Single Cell μ Power DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] PGND , [5] SW , [6] Vin , [7] GND , [8] COMP ,
[LTC3531-3.asy](#)

spice_model: LTC3531-3

Required Refdes Prefix: X

spice_lib: LTC3531-X.sub

Description: 200mA Buck-Boost Synchronous DC/DC Converter, Fixed 3V Output

Spice_Node_Sequence: [1] SW1 , [2] Vin , [3] GND , [4] _SHDN , [5] OUT , [6] SW2 , [7] PGND ,
[LTC3417A.asy](#)

spice_model: LTC3417A

Required Refdes Prefix: X

spice_lib: LTC3417A.sub

Description: Dual Synchronous 1.5A/1A 4MHz Step-Down DC/DC Regulator

Spice_Node_Sequence: [1] Run1 , [2] Vin1 , [3] Ith1 , [4] FB1 , [5] FB2 , [6] Ith2 , [7] Run2 , [8] Vin2 , [9] PGND2 , [10] Mode , [11] SW2 , [12] Pgood , [13] Freq , [14] GNDA , [15] Phase , [16] SW1 , [17] PGND1 ,
[LT3972.asy](#)

spice_model: LT3972

Required Refdes Prefix: X

spice_lib: LT3972.sub

Description: 33V, 3.5A, 2.4MHz Step-Down Switching Regulator with 75 μ A Quiescent Current

Spice_Node_Sequence: [1] BD , [2] Boost , [3] SW , [4] Vin , [5] Run/SS , [6] Pgood , [7] FB , [8] Vc , [9] Rt , [10] GND ,
[LT1938.asy](#)

spice_model: LT1938

Required Refdes Prefix: X

spice_lib: LT3684.sub

Description: 25V, 2.2A, 2.8MHz Step-Down Switching Regulator

Spice_Node_Sequence: [1] BD , [2] Boost , [3] SW , [4] Vin , [5] Run/SS , [6] Pgood , [7] Bias , [8] FB , [9] Vc , [10] Rt , [11] GND ,
[LTC3736-1.asy](#)

spice_model: LTC3736-1

spice_lib: LTC3736-1.sub

Required Refdes Prefix: X

Description: Dual 2-Phase, no Rsense(TM), Synchronous Controller with Spread Spectrum

Spice_Node_Sequence: [1] SW1 , [2] Iprg1 , [3] Vfb1 , [4] Ith1 , [5] Iprg2 , [6] FREQ , [7] SGND , [8] Vin , [9] Track , [10] Vfb2 , [11] Ith2 , [12] Pgood , [13] SW2 , [14] Sense2+ , [15] PGND2 , [16] BG2 , [17] Run/SS , [18] TG2 , [19] PGND1 , [20] TG1 , [21] SSDIS , [22] BG1 , [23] Sense1+ ,
[LT3695-3.3.asy](#)

spice_model: LT3695-3.3

Required Refdes Prefix: X

spice_lib: LT3695-x.sub

Description: 1A Fault Tolerant μ Power Step-Down Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] PGND , [2] DA , [3] SW , [4] Run/SS , [5] Rt , [6] SYNC , [7] Vin , [8] Vc , [9] PG , [10] GND , [11] OUT , [12] Boost ,
[LTC1174-3.3.asy](#)

spice_model: LTC1174-3.3

Required Refdes Prefix: X

spice_lib: LTC1174.sub

Description: High Efficiency Step-Down and Inverting DC/DC Converter, Fixed 3.3V

Spice_Node_Sequence: [1] FB , [2] LBout , [3] LBin , [4] GND , [5] SW , [6] Vin , [7] Ipgm , [8] _SHDN , [LTC1628.asy](#)

spice_model: LTC1628

Required Refdes Prefix: X

spice_lib: LTC1628.sub

Description: High Efficiency, 2-Phase Synchronous Step-Down Switching Regulator

Spice_Node_Sequence: [1] Run/SS1 , [2] Sense1+ , [3] Sense1- , [4] Vosense1 , [5] FreqSet , [6] STBYMD , [7] FCB , [8] Ith1 , [9] SGND , [10] 3.3Vout , [11] Ith2 , [12] Vosense2 , [13] Sense2- , [14] Sense2+ , [15] Run/SS2 , [16] TG2 , [17] SW2 , [18] Boost2 , [19] BG2 , [20] PGND , [21] INTVcc , [22] EXTVcc , [23] BG1 , [24] Vin , [25] Boost1 , [26] SW1 , [27] TG1 , [28] FLTCPL , [LTC1550L.asy](#)

spice_model: LTC1550L

Required Refdes Prefix: X

spice_lib: LTC1550L.sub

Description: Low Noise, Switched Capacitor Regulated Voltage Inverter, Adjustable Output

Spice_Node_Sequence: [1] NC , [2] C+ , [3] NC , [4] Vout , [5] C- , [6] PGND , [7] AGND , [8] NC , [9] NC , [10] NC , [11] CPout , [12] ADJ , [13] NC , [14] REG , [15] _SHDN , [16] Vin , [LT1086.asy](#)

spice_model: LT1086

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 1.5A Low Dropout Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LTC1702A.asy](#)

spice_model: LTC1702A

Required Refdes Prefix: X

spice_lib: LTC1702A.sub

Description: Dual 550kHz Synchronous 2-Phase Switching Regulator Controller\n\nNote: Only one phase of two phases is modeled.

Spice_Node_Sequence: [1] PVcc , [2] Boost , [3] BG , [4] TG , [5] SW , [6] Imax , [7] Pgood , [8] FCB , [9] Run/SS , [10] Comp , [11] SGND , [12] FB , [13] Vcc , [14] Fault , [15] PGND ,

[LT3756.asy](#)

spice_model: LT3756

Required Refdes Prefix: X

spice_lib: LT3756.sub

Description: Full-Featured LED Controller

Spice_Node_Sequence: [1] Vref , [2] PWMin , [3] _OPENLED , [4] SS , [5] Rt , [6] _SHDN/UVLO , [7] INTVcc , [8] Vin , [9] Sense , [10] Gate , [11] PWMout , [12] FB , [13] ISN , [14] ISP , [15] Vc , [16] CTRL , [17] GND ,

[LT1108-5.asy](#)

spice_model: LT1108-5

Required Refdes Prefix: X

spice_lib: LT1108.sub

Description: 5Volt μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] Sense ,

[LTC1706-81.asy](#)

spice_model: LTC1706-81

Required Refdes Prefix: X

spice_lib: LTC1706-81.sub

Description: 5-Bit Desktop VID Voltage Programmer

Spice_Node_Sequence: [1] VID2 , [2] VID3 , [3] Vcc , [4] GND , [5] FB , [6] Sense , [7] VID0 , [8] VID1 ,

[9] VID4 ,

[LT3519-2.asy](#)

spice_model: LT3519-2

Required Refdes Prefix: X

spice_lib: LT3519.sub

Description: LED Driver with Integrated Schottky Diode

Spice_Node_Sequence: [1] GND , [2] PWM , [3] _SHDN/UVLO , [4] Vin , [5] SW , [6] Anode , [7] Cathode , [8] ISP , [9] ISN , [10] FB , [11] CTRL , [12] Vref ,

[LTC3778.asy](#)

spice_model: LTC3778

Required Refdes Prefix: X

spice_lib: LTC3778.sub

Description: Wide Operating Range, No Rsense(TM) Step-Down Controller

Spice_Node_Sequence: [1] Run/SS , [2] Von , [3] Vrmg , [4] FCB , [5] Ith , [6] GND , [7] Ion , [8] FB , [9] EXTVcc , [10] Vin , [11] INTVcc , [12] BG , [13] PGND , [14] SW , [15] TG , [16] Boost , [17] Sense+ , [18] Pgood , [19] Sense- , [20] DRVcc ,

[LT3587.asy](#)

spice_model: LT3587

Required Refdes Prefix: X

Description: High Voltage Monolithic Inverter and Dual Boost

spice_lib: LT3587.sub

Spice_Node_Sequence: [1] Vout3 , [2] CAP3 , [3] SW3 , [4] GND , [5] _FLT , [6] FB2 , [7] SW2 , [8] SW1 , [9] CAP1 , [10] Vout1 , [11] FB1 , [12] EN/SS1 , [13] Vin , [14] EN/SS3 , [15] Ifb3 , [16] Vfb3 ,

[LT3474.asy](#)

spice_model: LT3474

Required Refdes Prefix: X

spice_lib: LT3474.sub

Description: Step-Down 1A LED Driver

Spice_Node_Sequence: [1] OUT , [2] LED , [3] Vin , [4] SW , [5] Boost , [6] BIAS , [7] GND , [8] Rt , [9] _SHDN , [10] Ref , [11] Vc , [12] Vadj , [13] PWM ,

[LTM8027.asy](#)

spice_model: LTM8027

Required Refdes Prefix: X

spice_lib: LTM8027.sub

Description: 60V, 4A DC/DC uModule Regulator

Spice_Node_Sequence: [1] Vin , [2] RUN , [3] SS , [4] Bias2 , [5] Adj , [6] Bias1 , [7] Vout , [8] Rt , [9] AUX , [10] GND ,

[LTC3618.asy](#)

spice_model: LTC3618

Required Refdes Prefix: X

spice_lib: LTC3618.sub

Description: Dual 4MHz, ±3A Synchronous Buck Converter for DDR Termination

Spice_Node_Sequence: [1] Phase , [2] FB2 , [3] Ith2 , [4] VDDQin , [5] SGND , [6] PVin2 , [7] SW2 , [8] Run2 , [9] Run1 , [10] Rt , [11] Pgood2 , [12] VTTR , [13] Pgood1 , [14] SW1 , [15] PVin1 , [16] SVin , [17] Trk/SS1 , [18] FB1 , [19] Ith1 , [20] Mode/Sync , [21] PGND ,

[LTC3890.asy](#)

spice_model: LTC3890

Required Refdes Prefix: X

spice_lib: LTC3890.sub

Description: 60V Low Iq, Dual, 2-Phase Synchronous Step-Down Controller

Spice_Node_Sequence: [1] Sense1- , [2] Freq , [3] PHASEMD , [4] CLKOUT , [5] PLLIN/Mode , [6] SGND , [7] Run1 , [8] Run2 , [9] Sense2- , [10] Sense2+ , [11] Vfb2 , [12] Ith2 , [13] Track/SS2 , [14] Pgood2 , [15] TG2 , [16] SW2 , [17] Boost2 , [18] BG2 , [19] INTVcc , [20] EXTVcc , [21] PGND , [22]

Vin , [23] BG1 , [24] Boost1 , [25] SW1 , [26] TG1 , [27] Pgood1 , [28] Ilim , [29] Track/SS1 , [30] Ith1 , [31] Vfb1 , [32] Sense1+ ,

[LT1935.asy](#)

spice_model: LT1935

Required Refdes Prefix: X

spice_lib: LT1935.sub

Description: 1.2MHz Boost DC/DC Converter in ThinSOT with 2A Switch

Spice_Node_Sequence: [1] SW , [2] GND , [3] FB , [4] _SHDN , [5] Vin ,

[LTC3851.asy](#)

spice_model: LTC3851

Required Refdes Prefix: X

spice_lib: LTC3851.sub

Description: Synchronous 40V Step-Down Switching Controller

Spice_Node_Sequence: [1] Mode/PLLIN , [2] Freq/PLLFLTR , [3] Run , [4] TK/SS , [5] Ith , [6] Vfb , [7] Sense- , [8] Sense+ , [9] Ilim , [10] GND , [11] BG , [12] IntVcc , [13] Vin , [14] Boost , [15] TG , [16] SW ,

[LT1965-2.5.asy](#)

spice_model: LT1965-2.5

Required Refdes Prefix: X

spice_lib: LT1965.lib

Description: 1.1A, Low Noise, Low Dropout Linear Regulator, Fixed 2.5V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,

[LTC3814-5.asy](#)

spice_model: LTC3814-5

Required Refdes Prefix: X

spice_lib: LTC3814-5.sub

Description: 60V Current Mode Synchronous Step-Up Controller

Spice_Node_Sequence: [1] Ioff , [2] Voff , [3] Vrng , [4] Pgood , [5] Ith , [6] FB , [7] Run/SS , [8] SGND , [9] NDRV , [10] EXTVcc , [11] INTVcc , [12] BG , [13] PGND , [14] SW , [15] TG , [16] Boost ,

[LT3467A.asy](#)

spice_model: LT3467A

Required Refdes Prefix: X

spice_lib: LT3467A.sub

Description: 2.1MHz, 1.1A Step-Up DC/DC Converter in ThinSOT with Integrated Soft-Start

Spice_Node_Sequence: [1] SW , [2] GND , [3] FB , [4] _SHDN , [5] SS , [6] Vin ,

[LTM4613.asy](#)

spice_model: LTM4613

Required Refdes Prefix: X

spice_lib: LTM4613.sub

Description: EN55022B Compliant 36Vin, 15Vout, 8A, DC/DC μ Module Regulator

Spice_Node_Sequence: [1] Vin , [2] PGND , [3] Vout , [4] VD , [5] DRVcc , [6] INTVcc , [7] PLLIN , [8] FCB , [9] Track/SS , [10] MPGM , [11] Fset , [12] FB , [13] MARG0 , [14] MARG1 , [15] SGND , [16] Comp , [17] Pgood , [18] Run ,

[LTC3411A.asy](#)

spice_model: LTC3411A

Required Refdes Prefix: X

spice_lib: LTC3411A.sub

Description: 1.25A, 4MHz, Synchronous Step-down DC/DC Converter

Spice_Node_Sequence: [1] SHDN/Rt , [2] Mode , [3] SGND , [4] SW , [5] PGND , [6] PVin , [7] SVin , [8] Pgood , [9] FB , [10] Ith ,

[LT1762-1.5.asy](#)

spice_model: LT1762-1.5

Required Refdes Prefix: X

spice_lib: LT1762.lib

Description: 150mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 1.5V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN ,

[LT3505.asy](#)

spice_model: LT3505

Required Refdes Prefix: X

spice_lib: LT3505.sub

Description: 1.2A Step-Down Switching Regulator in 3mm \times 3mm DFN

Spice_Node_Sequence: [1] Boost , [2] SW , [3] Vin , [4] _SHDN , [5] GND , [6] Rt , [7] FB , [8] Vc ,

[LTC1267-ADJ5.asy](#)

spice_model: LTC1267-ADJ5

Required Refdes Prefix: X

spice_lib: LTC1267-ADJ5.sub

Description: Dual High Efficiency Synchronous Step-Down Switching Regulator Controller, One Adjustable Output Voltage and One Fixed 5V Output

Spice_Node_Sequence: [1] Vin , [2] CAP1 , [3] P-gate1 , [4] P-drive1 , [5] N-gate1 , [6] FB , [7] Vcc1 , [8] Ct1 , [9] Ith1 , [10] SGND1 , [11] SHDN1 , [12] Sense-1 , [13] Sense+1 , [14] Ith5 , [15] Ct5 , [16] Sense-5 , [17] Sense+5 , [18] SHDN5 , [19] SGND5 , [20] Vcc5 , [21] PGND , [22] N-gate5 , [23] P-drive5 , [24] P-gate5 , [25] CAP5 , [26] MSHDN , [27] EXTVcc ,

[LT1085.asy](#)

spice_model: LT1085

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 3A Low Dropout Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LTC3705.asy](#)

spice_model: LTC3705

Required Refdes Prefix: X

spice_lib: LTC3705.sub

Description: Two-Switch Forward Controller and Gate Driver\n\nNote: Fault Transmit and Receive are not modeled as is not slave mode.

Spice_Node_Sequence: [1] GND , [2] IS , [3] VSLMT , [4] UVLO , [5] SSFLT , [6] Ndrv , [7] FB/IN+ , [8] FS/IN- , [9] PGND , [10] BG , [11] Vcc , [12] Boost , [13] TG , [14] TS ,

[LTC1255.asy](#)

spice_model: LTC1255

Required Refdes Prefix: X

spice_lib: LTC1255.sub

Description: Dual 24V High-Side MOSFET Driver

Spice_Node_Sequence: [1] DS1 , [2] G1 , [3] GND , [4] IN1 , [5] IN2 , [6] Vs , [7] G2 , [8] DS2 ,

[LTC1772.asy](#)

spice_model: LTC1772

Required Refdes Prefix: X

spice_lib: LTC1772.sub

Description: Constant Frequency Current Mode Step-Down DC/DC Controller in SOT-23

Spice_Node_Sequence: [1] Ith/Run , [2] GND , [3] FB , [4] Sense- , [5] Vin , [6] Pgate ,

[LTC3832-1.asy](#)

spice_model: LTC3832-1

Required Refdes Prefix: X

spice_lib: LTC3832-1.sub

Description: High Power Step-Down Synchronous DC/DC Controller for Low Voltage Operation

Spice_Node_Sequence: [1] G1 , [2] PVcc1 , [3] GND , [4] FB , [5] SS , [6] Comp , [7] Vcc/PVcc2 , [8] G2 ,

[LTC3810-5.asy](#)

spice_model: LTC3810-5

Required Refdes Prefix: X

spice_lib: LTC3810-5.sub

Description: 60V Current Mode Synchronous Switching Regulator Controller

Spice_Node_Sequence: [1] Von , [2] Vrng , [3] Pgood , [4] MODE/SYNC , [5] Ith , [6] FB , [7] PLL/LPF , [8] SS/TRACK , [9] _SHDN , [10] UVIN , [11] NDRV , [12] EXTVcc , [13] INTVcc , [14] DRVcc , [15] BG , [16] BGRTN , [17] Sense- , [18] Sense+ , [19] SW , [20] TG , [21] Boost , [22] Ion , [23] SGND , [LT1725.asy](#)

spice_model: LT1725

Required Refdes Prefix: X

spice_lib: LT1725.sub

Description: General Purpose Isolated Flyback Controller

Spice_Node_Sequence: [1] PGND , [2] Isense , [3] SFST , [4] Rocmp , [5] Rcmpc , [6] OSCAP , [7] Vc , [8] FB , [9] 3Vout , [10] UVLO , [11] SGND , [12] MinEnab , [13] EnDly , [14] Ton , [15] Vcc , [16] GATE ,

[LT1111.asy](#)

spice_model: LT1111

Required Refdes Prefix: X

spice_lib: LT1111.sub

Description: Adjustable μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] FB , [LTM8048.asy](#)

spice_model: LTM8048

Required Refdes Prefix: X

spice_lib: LTM8048.sub

Description: 3.1Vin to 32Vin Isolated μ Module DC/DC Converter with LDO Post Regulator

Spice_Node_Sequence: [1] GND , [2] Vout1 , [3] Vout- , [4] Adj1 , [5] Vin , [6] Bias , [7] Run , [8] SS , [9] BYP , [10] Vout2 , [11] Adj2 ,

[LTC3410-1.2.asy](#)

spice_model: LTC3410-1.2

Required Refdes Prefix: X

spice_lib: LTC3410-x.x.sub

Description: 2.25MHz, 300mA, Synchronous Step-Down Regulator in SC70, Fixed 1.2V Output

Spice_Node_Sequence: [1] Run , [2] GND , [3] SW , [4] Vin , [5] Vsense ,

[LTC3452.asy](#)

spice_model: LTC3452

Required Refdes Prefix: X

spice_lib: LTC3452.sub

Description: Synchronous Buck-Boost MAIN/CAMERA White LED Driver

Spice_Node_Sequence: [1] Vin , [2] ENL , [3] IsetL , [4] LEDL1 , [5] LEDL2 , [6] LEDL3 , [7] LEDL4 , [8] LEDL5 , [9] GND , [10] LEDH1 , [11] LEDH2 , [12] IsetH , [13] ENH , [14] Vc , [15] Vout , [16] SW2 , [17] PGND , [18] SW1 , [19] PVin ,

[LTC1436A.asy](#)

spice_model: LTC1436A

Required Refdes Prefix: X

spice_lib: LTC1436A.sub

Description: High Efficiency Low Noise Synchronous Step-Down Switching Regulator

Spice_Node_Sequence: [1] Cosc , [2] Run/SS , [3] LBO , [4] LBI , [5] Ith , [6] SFB , [7] SGND , [8] Vprog , [9] Vosense , [10] Sense- , [11] Sense+ , [12] AUXON , [13] AUXFB , [14] AUXDR , [15] ExtVcc , [16] PGND , [17] BG , [18] IntVcc , [19] Vin , [20] TGS , [21] SW , [22] TGL , [23] Boost ,

[LTC3240-2.5.asy](#)

spice_model: LTC3240-2.5

Required Refdes Prefix: X

spice_lib: LTC3240-2.5.sub

Description: 2.5V Step-up Step-down Charge Pump DC/DC Converter

Spice_Node_Sequence: [1] GND , [2] Vin , [3] Vout , [4] C+ , [5] C- , [6] _SHDN ,
[LTC3708.asy](#)

spice_model: LTC3708

Required Refdes Prefix: X

spice_lib: LTC3708.sub

Description: Fast 2-Phase, No Rsense Buck Controller with Output Tracking

Spice_Node_Sequence: [1] RUN/SS , [2] Ith1 , [3] FB1 , [4] Track1 , [5] SGND , [6] Track2 , [7] FB2 , [8] Ith2 , [9] ExtLPF , [10] IntLPF , [11] Vrng2 , [12] Ion2 , [13] Boost2 , [14] TG2 , [15] SW2 , [16] Sense2+ , [17] Vcc , [18] Sense2- , [19] PGND2 , [20] BG2 , [21] DRVcc , [22] BG1 , [23] PGND1 , [24] Sense1- , [25] Sense1+ , [26] SW1 , [27] TG1 , [28] Boost1 , [29] Ion1 , [30] Pgood , [31] FCB , [32] Vrng1 ,
[LTC3890-1.asy](#)

spice_model: LTC3890-1

Required Refdes Prefix: X

spice_lib: LTC3890-1.sub

Description: 60V Low Iq, Dual, 2-Phase Synchronous Step-Down Controller

Spice_Node_Sequence: [1] Ith1 , [2] Vfb1 , [3] Sense1+ , [4] Sense1- , [5] Freq , [6] PLLIN/Mode , [7] SGND , [8] Run1 , [9] Run2 , [10] Sense2- , [11] Sense2+ , [12] Vfb2 , [13] Ith2 , [14] Track/SS2 , [15] TG2 , [16] SW2 , [17] Boost2 , [18] BG2 , [19] INTVcc , [20] EXTVcc , [21] PGND , [22] Vin , [23] BG1 , [24] Boost1 , [25] SW1 , [26] TG1 , [27] Pgood1 , [28] Track/SS1 ,
[LTC1626.asy](#)

spice_model: LTC1626

Required Refdes Prefix: X

spice_lib: LTC1626.sub

Description: Low Voltage, High Efficiency Step-Down DC/DC Controller, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vin , [2] LBout , [3] LBin , [4] Ct , [5] Ith , [6] Sense- , [7] Sense+ , [8] FB , [9] SHDN , [10] GND , [11] SW ,
[LTC1142.asy](#)

spice_model: LTC1142

Required Refdes Prefix: X

spice_lib: LTC1142.sub

Description: Dual High Efficiency Synchronous Step-Down Switching Regulator Controller, Fixed 3.3V and 5V Outputs

Spice_Node_Sequence: [1] Sense+3 , [2] SHDN3 , [3] SGND3 , [4] PGND3 , [5] N-drive3 , [6] P-drive5 , [7] Vin5 , [8] Ct5 , [9] Ith5 , [10] Sense-5 , [11] Sense+5 , [12] SHDN5 , [13] SGND5 , [14] PGND5 , [15] N-drive5 , [16] P-drive3 , [17] Vin3 , [18] Ct3 , [19] Ith3 , [20] Sense-3 ,
[LT3495-1.asy](#)

spice_model: LT3495-1

Required Refdes Prefix: X

spice_lib: LT3495-1.sub

Description: 350mA μ Power Low Noise Boost Converter with Output Disconnect

Spice_Node_Sequence: [1] GND , [2] Vcc , [3] CTRL , [4] _SHDN , [5] FB , [6] Vout , [7] CAP , [8] SW ,
[LTC3865-1.asy](#)

spice_model: LTC3865-1

Required Refdes Prefix: X

spice_lib: LTC3865-1.sub

Description: Dual, 2-Phase Synchronous DC/DC Controller with Pin Selectable Outputs

Spice_Node_Sequence: [1] Run1 , [2] Sense1+ , [3] Sense1- , [4] Vosense1 , [5] TK/SS1 , [6] Ith1 , [7] SGND , [8] VID11 , [9] VID12 , [10] Ith2 , [11] TK/SS2 , [12] Vosense2 , [13] Sense2- , [14] Sense2+ , [15] Run2 , [16] VID21 , [17] Pgood2 , [18] Pgood1 , [19] SW2 , [20] TG2 , [21] Boost2 , [22] PGND2 , [23] BG2 , [24] ExtVcc , [25] IntVcc , [26] Vin , [27] BG1 , [28] PGND1 , [29] Boost1 , [30] TG1 , [31] SW1 , [32] PLLIN/Mode , [33] VID22 , [34] Freq ,

[LT1767-5.asy](#)

spice_model: LT1767-5

Required Refdes Prefix: X

spice_lib: LT1767-x.x.sub

Description: Monolithic 1.5A, 1.25MHz Step-Down Switching Regulator, Fixed 5V Output\n\nNote: Sync pin is not modeled.

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] GND , [5] _SHDN , [6] FB , [7] Vc ,

[LTC3527.asy](#)

spice_model: LTC3527

Required Refdes Prefix: X

spice_lib: LTC3527.sub

Description: Dual 800mA/400mA, 1.2MHz/2.2MHz Synchronous Step-Up DC/DC Converter

Spice_Node_Sequence: [1] _SHDN1 , [2] FB1 , [3] Mode , [4] Vin1 , [5] Vout1 , [6] SW1 , [7] SW2 , [8] Vout2 , [9] Vin2 , [10] Fsel , [11] FB2 , [12] _SHDN2 , [13] Pgood2 , [14] GND , [15] Vin , [16] Pgood1 ,

[LT1930.asy](#)

spice_model: LT1930

Required Refdes Prefix: X

spice_lib: LT1930.sub

Description: 1A, 1.2MHz Boost DC/DC Converter in ThinSOT

Spice_Node_Sequence: [1] SW , [2] GND , [3] FB , [4] _SHDN , [5] Vin ,

[LT1109-12.asy](#)

spice_model: LT1109-12

Required Refdes Prefix: X

spice_lib: LT1109.sub

Description: μ Power Low Cost DC/DC Converter, Fixed 12V

Spice_Node_Sequence: [1] Vin , [2] SW , [3] GND , [4] _SDN , [5] Sense ,

[LT3755-2.asy](#)

spice_model: LT3755-2

Required Refdes Prefix: X

spice_lib: LT3755.sub

Description: Full-Featured LED Controller

Spice_Node_Sequence: [1] Vref , [2] PWMin , [3] _OPENLED , [4] SS , [5] Rt , [6] _SHDN/UVLO , [7] INTVcc , [8] Vin , [9] Sense , [10] Gate , [11] PWMout , [12] FB , [13] ISN , [14] ISP , [15] Vc , [16] CTRL , [17] GND ,

[LT6105.asy](#)

spice_model: LT6105

Required Refdes Prefix: X

spice_lib: LT6105.sub

Description: Precision, Extended Input Range Current Sense Amplifier

Spice_Node_Sequence: [1] IN- , [2] V+ , [3] V- , [4] Vout , [5] IN+ ,

[LT1172.asy](#)

spice_model: LT1172

Required Refdes Prefix: X

spice_lib: LT1172.sub

Description: 100kHz, 1.25A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LTC1159-5.asy](#)

spice_model: LTC1159-5

Required Refdes Prefix: X

spice_lib: LTC1159-5.sub

Description: High Efficiency Synchronous Step-Down Switching Regulator Controller, Fixed 5V Output

Spice_Node_Sequence: [1] P-Gate , [2] Vin , [3] Vcc , [4] P-Drive , [5] Ct , [6] Ith , [7] Sense- , [8] Sense+ , [9] SHDN1 , [10] SGND , [11] PGND , [12] N-Gate , [13] EXTVcc , [14] SHDN2 , [15] CAP ,

[LT1776.asy](#)

spice_model: LT1776

Required Refdes Prefix: X

spice_lib: LT1776.sub

Description: Wide Input Range, High Efficiency, Step-Down Switching Regulator

Spice_Node_Sequence: [1] _SHDN , [2] Vcc , [3] SW , [4] GND , [5] Vin , [6] FB , [7] Vc ,

[LT3493-3.asy](#)

spice_model: LT3493-3

Required Refdes Prefix: X

spice_lib: LT3493-3.sub

Description: 1.2A, 750KHz Step-Down Switching Regulator in 2mm × 3mm DFN

Spice_Node_Sequence: [1] FB , [2] GND , [3] Boost , [4] SW , [5] Vin , [6] _SHDN ,

[LT1963A.asy](#)

spice_model: LT1963A

Required Refdes Prefix: X

spice_lib: LT1963.lib

Description: 1.5A, Low Noise, Fast Transient Response LDO Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] ADJ ,

[LT3484-0.asy](#)

spice_model: LT3484-0

Required Refdes Prefix: X

spice_lib: LT3484-0.sub

Description: Photoflash Capacitor Charger

Spice_Node_Sequence: [1] _Done , [2] Charge , [3] Vin , [4] SW , [5] Vbat , [6] GND ,

[LT1947.asy](#)

spice_model: LT1947

Required Refdes Prefix: X

spice_lib: LT1947.sub

Description: Adjustable Output TFT-LCD Triple Switching Regulator

Spice_Node_Sequence: [1] FB1 , [2] FB2 , [3] Ct , [4] SW1 , [5] GND , [6] Vin , [7] SW2 , [8] _SHDN , [9] Vo2 , [10] Von ,

[LTC1627.asy](#)

spice_model: LTC1627

Required Refdes Prefix: X

spice_lib: LTC1627.sub

Description: Monolithic Synchronous Step-Down Switching Regulator

Spice_Node_Sequence: [1] Ith , [2] Run/SS , [3] FB , [4] GND , [5] SW , [6] Vin , [7] Vdr , [8] FCB ,

[LTC3528.asy](#)

spice_model: LTC3528

Required Refdes Prefix: X

spice_lib: LTC3528.sub

Description: 1A, 1MHz Synchronous Step-up DC/DC Converter in 3mm×2mm DFN

Spice_Node_Sequence: [1] _SHDN , [2] FB , [3] Pgood , [4] Vout , [5] SW , [6] PGND , [7] SGND , [8] Vin ,

[LTC1530-3.3.asy](#)

spice_model: LTC1530-3.3

Required Refdes Prefix: X

spice_lib: LTC1530.sub

Description: High Power Synchronous Switching Regulator Controller, Fixed 3.3 Volt Output

Spice_Node_Sequence: [1] PVcc , [2] GND , [3] Sense , [4] Comp , [5] Imax , [6] Ifb , [7] G2 , [8] G1 ,

[LT3579-1.asy](#)

spice_model: LT3579-1

Required Refdes Prefix: X

spice_lib: LT3579.sub

Description: 6A Boost/Inverting DC/DC Converter with Fault Protection\n\nSYNC and CLKOUT pins are not modeled

Spice_Node_Sequence: [1] FB , [2] Vc , [3] Gate , [4] _Fault , [5] Vin , [6] SW1 , [7] SW2 , [8] _SHDN , [9] Rt , [10] SS , [11] GND ,

[LTC1707.asy](#)

spice_model: LTC1707

Required Refdes Prefix: X

spice_lib: LTC1707.sub

Description: High Efficiency Monolithic Synchronous Step-Down Regulator\n\nNote: External sync. capability is not modeled.

Spice_Node_Sequence: [1] Ith , [2] Run/SS , [3] FB , [4] GND , [5] SW , [6] Vin , [7] Mode , [8] Vref ,

[LTC3410-1.875.asy](#)

spice_model: LTC3410-1.875

Required Refdes Prefix: X

spice_lib: LTC3410-x.x.sub

Description: 2.25MHz, 300mA, Synchronous Step-Down Regulator in SC70, Fixed 1.875V Output

Spice_Node_Sequence: [1] Run , [2] GND , [3] SW , [4] Vin , [5] Vsense ,

[LT1303.asy](#)

spice_model: LT1303

Required Refdes Prefix: X

spice_lib: LT1303.sub

Description: μ Power High Efficiency DC/DC Converter with Low-Battery Detector, adjustable output voltage

Spice_Node_Sequence: [1] GND , [2] LBO , [3] SHDN , [4] FB , [5] LBI , [6] Vin , [7] SW , [8] PGND ,

[LTC4449.asy](#)

spice_model: LTC4449

Required Refdes Prefix: X

spice_lib: LTC4449.sub

Description: High Speed Synchronous N-Channel MOSFET Driver

Spice_Node_Sequence: [1] TG , [2] TS , [3] BG , [4] GND , [5] IN , [6] Vlogic , [7] Vcc , [8] Boost ,

[LTC1165.asy](#)

spice_model: LTC1165

Required Refdes Prefix: X

spice_lib: LTC1165.sub

Description: Triple 1.8V to 6V High-Side MOSFET Driver

Spice_Node_Sequence: [1] IN1 , [2] IN2 , [3] IN3 , [4] GND , [5] OUT3 , [6] OUT2 , [7] OUT1 , [8] Vs ,

[LTC3808.asy](#)

spice_model: LTC3808

spice_lib: LTC3808.sub

Required Refdes Prefix: X

Description: No Rsense(TM), Low EMI, Synchronous DC/DC Controller with Output Tracking\n\nNote: Short circuit protection is not modeled.

Spice_Node_Sequence: [1] PLLLPF , [2] SYNC/Mode , [3] Track/SS , [4] Pgood , [5] Vfb , [6] Ith , [7] Run , [8] Iprg , [9] BG , [10] TG , [11] Sense+ , [12] Vin , [13] Sense- , [14] SW , [15] GND ,

[LT1585-3.3.asy](#)

spice_model: LT1585-3.3

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT3023.asy](#)

spice_model: LT3023

Required Refdes Prefix: X

spice_lib: LT3023.lib

Description: Dual 100mA, Low Dropout, Low Noise, μ Power Regulator

Spice_Node_Sequence: [1] BYP2 , [2] ADJ2 , [3] GND , [4] ADJ1 , [5] BYP1 , [6] OUT1 , [7] _SHDN1 , [8] IN , [9] _SHDN2 , [10] OUT2 ,

[LTC1754-5.asy](#)

spice_model: LTC1754-5

Required Refdes Prefix: X

spice_lib: LTC1754-5.sub

Description: μ Power, Regulated 5V Charge Pump with Shutdown in SOT-23

Spice_Node_Sequence: [1] Vout , [2] GND , [3] _SHDN , [4] C- , [5] Vin , [6] C+ ,

[LT1513-2.asy](#)

spice_model: LT1513-2

Required Refdes Prefix: X

spice_lib: LT1513-2.sub

Description: SEPIC Constant- or Constant Current/Constant Voltage Battery Charger

Spice_Node_Sequence: [1] Vc , [2] FB , [3] IFB , [4] GND , [5] SW , [6] _SHDN , [7] Vin ,

[LT1086H.asy](#)

spice_model: LT1086H

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 0.7A Low Dropout Positive Adjustable Regulator

Spice_Node_Sequence: [1] IN , [2] ADJ , [3] OUT ,

[LTC1504A-3.3.asy](#)

spice_model: LTC1504-3.3A

Required Refdes Prefix: X

spice_lib: LTC1504A.sub

Description: 500mA Low Voltage Step-Down Synchronous Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] I_{max} , [2] Vcc , [3] SW , [4] GND , [5] Sense , [6] _SHND , [7] SS , [8] COMP ,

[LTC3210-1.asy](#)

spice_model: LTC3210-1

Required Refdes Prefix: X

spice_lib: LTC3210-1.sub

Description: MAIN/CAM LED Controller with 64-Step Brightness Control in 3mm*3mm QFN

Spice_Node_Sequence: [1] C1P , [2] CPO , [3] ENM , [4] MLED1 , [5] MLED2 , [6] MLED3 , [7] MLED4 , [8] RM , [9] RC , [10] ENC , [11] CLED , [12] GND , [13] C2M , [14] C1M , [15] VBAT , [16] C2P ,

[LTC3541-2.asy](#)

spice_model: LTC3541-2

Required Refdes Prefix: X

Description: High Efficiency Buck + VLDO Regulator

spice_lib: LTC3541-2.sub

Spice_Node_Sequence: [1] Vin , [2] ENBuck , [3] Vout , [4] LVout , [5] LVin , [6] GND , [7] Mode , [8] ENVLDO , [9] SW ,

[LTC3407-4.asy](#)

spice_model: LTC3407-4

Required Refdes Prefix: X

spice_lib: LTC3407-2.sub

Description: Dual Synchronous, 800mA, 2.25MHz Step-Down DC/DC Regulator\n\nNote: POR is not modeled

Spice_Node_Sequence: [1] FB1 , [2] Run1 , [3] Vin , [4] SW1 , [5] GND , [6] Mode , [7] SW2 , [8] Run2 , [9] FB2 ,

[LT3485-1.asy](#)

spice_model: LT3485-1

Required Refdes Prefix: X

spice_lib: LT3485-1.sub

Description: Photoflash Capacitor Charger with Output Voltage Monitor and Integrated IGBT Drive -- 550mA typ Peak Switch Current

Spice_Node_Sequence: [1] Charge , [2] Vbat , [3] Vin , [4] SW , [5] IBGTout , [6] IBGTin , [7] IBGTpwr , [8] _Done , [9] Vmont , [10] GND ,

[LT1934.asy](#)

spice_model: LT1934

Required Refdes Prefix: X

spice_lib: LT1934.sub

Description: μ Power Step-Down Switching Regulator in ThinSOT

Spice_Node_Sequence: [1] Boost , [2] GND , [3] FB , [4] _SHDN , [5] Vin , [6] SW ,

[LTM8020.asy](#)

spice_model: LTM8020

Required Refdes Prefix: X

spice_lib: LTM8020.sub

Description: 200mA, 36V DC/DC μ Module

Spice_Node_Sequence: [1] Vin , [2] _SHDN , [3] GND , [4] ADJ , [5] BIAS , [6] OUT ,

[LT3485-3.asy](#)

spice_model: LT3485-3

Required Refdes Prefix: X

spice_lib: LT3485-3.sub

Description: Photoflash Capacitor Charger with Output Voltage Monitor and Integrated IGBT Drive -- 1.7A typ peak Switch Current

Spice_Node_Sequence: [1] Charge , [2] Vbat , [3] Vin , [4] SW , [5] IBGTout , [6] IBGTin , [7] IBGTpwr , [8] _Done , [9] Vmont , [10] GND ,

[LTC3541.asy](#)

spice_model: LTC3541

Required Refdes Prefix: X

Description: High Efficiency Buck + VLDO Regulator

spice_lib: LTC3541.sub

Spice_Node_Sequence: [1] Vin , [2] ENBuck , [3] BuckFB , [4] LFB , [5] LVout , [6] LVin , [7] GND , [8] Mode , [9] ENVLDO , [10] SW ,

[LT3436.asy](#)

spice_model: LT3436

Required Refdes Prefix: X

spice_lib: LT3436.sub

Description: 3A, 800KHz Switching Regulator

Spice_Node_Sequence: [1] Vin , [2] SW , [3] GND , [4] _SHDN , [5] FB , [6] Vc ,

[LTM4612.asy](#)

spice_model: LTM4612

Required Refdes Prefix: X

spice_lib: LTM4612.sub

Description: Ultralow Noise 36Vin, 15Vout, 5A, DC/DC μ Module

Spice_Node_Sequence: [1] Vin , [2] PGND , [3] Vout , [4] VD , [5] DRVcc , [6] INTVcc , [7] PLLIN , [8] FCB , [9] Track/SS , [10] MPGM , [11] Fset , [12] FB , [13] MARG0 , [14] MARG1 , [15] SGND , [16] Comp , [17] Pgood , [18] Run ,

[LTC3225-1.asy](#)

spice_model: LTC3225-1

Required Refdes Prefix: X

spice_lib: LTC3225-1.sub

Description: 150mA Supercapacitor Charger

Spice_Node_Sequence: [1] C+ , [2] C- , [3] Cx , [4] _SHDN , [5] Pgood , [6] Vsel , [7] Prog , [8] GND , [9] Vin , [10] Cout ,

[LTC1550.asy](#)

spice_model: LTC1550

Required Refdes Prefix: X

spice_lib: LTC1550.sub

Description: Low Noise, Switched Capacitor Regulated Voltage Inverter, Adjustable Output

Spice_Node_Sequence: [1] NC , [2] C+ , [3] NC , [4] Vout , [5] C- , [6] PGND , [7] AGND , [8] NC , [9] NC , [10] NC , [11] CPout , [12] ADJ , [13] Sense , [14] REG , [15] _SHDN , [16] Vin ,

[LT1129-3.3.asy](#)

spice_model: LT1129-3.3

Required Refdes Prefix: X

spice_lib: LT1129.lib

Description: μ Power Low Dropout Regulator with Shutdown, Fixed 3.3V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] _SHDN , [5] IN ,

[LT1962-3.3.asy](#)

spice_model: LT1962-3.3

Required Refdes Prefix: X

spice_lib: LT1962.lib

Description: 300mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 3.3V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN ,

[LTC3834.asy](#)

spice_model: LTC3834

Required Refdes Prefix: X

spice_lib: LTC3834.sub

Description: 30 μ A Iq Synchronous Step-Down Controller

Spice_Node_Sequence: [1] CLKOUT , [2] PLLLPF , [3] Ith , [4] Track/SS , [5] FB , [6] SGND , [7] PGND , [8] BG , [9] INTVcc , [10] EXTVcc , [11] Vin , [12] SW , [13] TG , [14] Boost , [15] Run , [16] Sense- , [17] Sense+ , [18] Pgood , [19] PLLIN/Mode , [20] PHASMD ,

[LTC3862.asy](#)

spice_model: LTC3862

Required Refdes Prefix: X

spice_lib: LTC3862.sub

Description: Multi-Phase Current Mode Step-Up DC/DC Controller

Spice_Node_Sequence: [1] Dmax , [2] Slope , [3] Blank , [4] PhaseMode , [5] Freq , [6] SS , [7] Ith , [8] FB , [9] SGND , [10] CLKOut , [11] SYNC , [12] PLLFLTR , [13] Sense2+ , [14] Sense2- , [15] Gate2 , [16] PGND , [17] Gate1 , [18] INTVcc , [19] Vin , [20] Run , [21] Sense1- , [22] Sense1+ , [23] 3V8 ,

[LTC3812-5.asy](#)

spice_model: LTC3812-5

Required Refdes Prefix: X

spice_lib: LTC3812-5.sub

Description: 60V Current Mode Synchronous Switching Regulator Controller

Spice_Node_Sequence: [1] Ion , [2] Vrng , [3] Pgood , [4] FCB , [5] Ith , [6] FB , [7] Run/SS , [8] SGND , [9] NDRV , [10] EXTVcc , [11] INTVcc , [12] BG , [13] PGND , [14] SW , [15] TG , [16] Boost ,

[LT3837.asy](#)

spice_model: LT3837

Required Refdes Prefix: X

spice_lib: LT3837.sub

Description: Isolated No-Opto Synchronous Flyback Controller

Spice_Node_Sequence: [1] SG , [2] Vcc , [3] Ton , [4] ENDLY , [5] SYNC , [6] SFST , [7] OSC , [8] FB , [9] Vc , [10] UVLO , [11] Sense- , [12] Sense+ , [13] Ccmp , [14] Rcmp , [15] PGDLY , [16] PG , [17] GND ,

[LTC1174HV-3.3.asy](#)

spice_model: LTC1174HV-3.3

Required Refdes Prefix: X

spice_lib: LTC1174.sub

Description: High Efficiency Step-Down and Inverting DC/DC Converter, Fixed 3.3V

Spice_Node_Sequence: [1] FB , [2] LBout , [3] LBin , [4] GND , [5] SW , [6] Vin , [7] Ipgm , [8] _SHDN , [LT1790-2.048.asy](#)

spice_model: LT1790-2.048

Required Refdes Prefix: X

spice_lib: LT1790.lib

Description: μ Power SOT-23 Low Dropout 2.048V Reference

Spice_Node_Sequence: [1] IN , [2] GND , [3] OUT , [LT1587-3.45.asy](#)

spice_model: LT1587-3.45

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Regulator, Fixed 3.45V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN , [LTC2903-B1.asy](#)

spice_model: LTC2903-B1

Required Refdes Prefix: X

spice_lib: LTC2903-B1.sub

Description: Precision Quad Supply Monitor in 6-Lead SOT-23

Spice_Node_Sequence: [1] V1 , [2] GND , [3] V2 , [4] V3 , [5] V4 , [6] _RST , [LTC1148HV-3.3.asy](#)

spice_model: LTC1148HV-3.3

Required Refdes Prefix: X

spice_lib: LTC1148-3.3.sub

Description: High Efficiency Synchronous Step-Down Switching Regulator Controller, Fixed 3.3V Output

Spice_Node_Sequence: [1] P-drive , [2] Vin , [3] Ct , [4] Ith , [5] Sense- , [6] Sense+ , [7] SHDN , [8] SGND , [9] PGND , [10] N-drive ,

[LT3487.asy](#)

spice_model: LT3487

Required Refdes Prefix: X

spice_lib: LT3487.sub

Description: Boost and Inverting Switching Regulator for CCD Bias

Spice_Node_Sequence: [1] CAP , [2] SWP , [3] Vbat , [4] SWN , [5] DN , [6] Vin , [7] FBN , [8] Run/SS , [9] FBP , [10] Vpos , [11] GND ,

[LTC3035.asy](#)

spice_model: LTC3035

Required Refdes Prefix: X

spice_lib: LTC3035.sub

Description: 300mA VLDO Linear Regulator with Charge Pump Bias Generator

Spice_Node_Sequence: [1] CP , [2] CM , [3] GND , [4] IN , [5] OUT , [6] ADJ , [7] _SHDN , [8] BIAS , [LTC3251.asy](#)

spice_model: LTC3251

Required Refdes Prefix: X

spice_lib: LTC3251.sub

Description: 500mA High Efficiency, Low Noise, Inductorless Step-Down DC/DC Converter

Spice_Node_Sequence: [1] MD0 , [2] Vin , [3] C1+ , [4] C1- , [5] GND , [6] C2- , [7] Vout , [8] C2+ , [9] MD1 , [10] FB ,

[LT1304.asy](#)

spice_model: LT1304

Required Refdes Prefix: X

spice_lib: LT1304.sub

Description: μ Power DC/DC Converter with Low-Battery Detector Active in Shutdown, Adjustable output voltage

Spice_Node_Sequence: [1] LBI , [2] LBO , [3] Vin , [4] SW , [5] GND , [6] Ilim , [7] _SHDN , [8] FB , [LT1962-1.5.asy](#)

spice_model: LT1962-1.5

Required Refdes Prefix: X

spice_lib: LT1962.lib

Description: 300mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 1.5V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN , [LT1962-3.asy](#)

spice_model: LT1962-3

Required Refdes Prefix: X

spice_lib: LT1962.lib

Description: 300mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 3V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN , [LTC3418.asy](#)

spice_model: LTC3418

Required Refdes Prefix: X

spice_lib: LTC3418.sub

Description: 8A, 4MHz, Monolithic Synchronous Step-down Regulator\n\nNote: Vref pin is not modeled and is assumed to be bypassed. External Sync is also not modeled

Spice_Node_Sequence: [1] Rt , [2] Mode , [3] SGND , [4] SW , [5] PGND , [6] PVin , [7] SVin , [8] Pgood , [9] FB , [10] Ith , [11] Run/SS , [12] Track ,

[LTC3805-5.asy](#)

spice_model: LTC3805-5

Required Refdes Prefix: X

spice_lib: LTC3805-5.sub

Description: Adjustable Frequency Current Mode Flyback/Boost/SEPIC DC/DC Controller

Spice_Node_Sequence: [1] SSFLT , [2] Ith , [3] FB , [4] Run , [5] FS , [6] Isense , [7] OC , [8] Vcc , [9] Gate , [10] GND ,

[LT1963A-1.8.asy](#)

spice_model: LT1963A-1.8

Required Refdes Prefix: X

spice_lib: LT1963.lib

Description: 1.5A, Low Noise, Fast Transient Response LDO Regulator, Fixed 1.8V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] SENSE , [LT3014B.asy](#)

spice_model: LT3014B

Required Refdes Prefix: X

spice_lib: LT3014.lib

Description: 20mA, 3V to 80V Low Dropout μ Power Linear Regulator

Spice_Node_Sequence: [1] OUT , [2] ADJ , [3] GND , [4] IN , [LTC3891.asy](#)

spice_model: LTC3891

Required Refdes Prefix: X

spice_lib: LTC3891.sub

Description: Low Iq, 60V Synchronous Step-Down Controller

Spice_Node_Sequence: [1] Sense- , [2] Freq , [3] PLLIN/Mode , [4] SGND , [5] Run , [6] INTVcc , [7] EXTVcc , [8] PGND , [9] Vin , [10] BG , [11] Boost , [12] SW , [13] TG , [14] Pgood , [15] Ilim , [16] Track/SS , [17] Ith , [18] Vfb , [19] Sense+ ,

[LTM8047.asy](#)

spice_model: LTM8047

Required Refdes Prefix: X

spice_lib: LTM8047.sub

Description: 3.1Vin to 32Vin Isolated μ Module DC/DC Converter

Spice_Node_Sequence: [1] GND , [2] Vout+ , [3] Vout- , [4] Adj , [5] Vin , [6] Bias , [7] Run , [8] SS ,
[LTC3856.asy](#)

spice_model: LTC3856

Required Refdes Prefix: X

spice_lib: LTC3856.sub

Description: 2-Phase Synchronous Step-Down DC/DC Controller with Diffamp

Spice_Node_Sequence: [1] Freq , [2] Run , [3] Sense1+ , [4] Sense1- , [5] TK/SS , [6] FB , [7] Ith , [8] SGND , [9] AVP , [10] Itemp , [11] PhaseMD , [12] Sense2+ , [13] Sense2- , [14] Diffp , [15] Diffn , [16] Diffout , [17] Iset , [18] Ilim , [19] Mode , [20] Pgood , [21] SW2 , [22] TG2 , [23] Boost2 , [24] PGND2 , [25] BG2 , [26] ExtVcc , [27] IntVcc , [28] Vin , [29] BG1 , [30] PGND1 , [31] Boost1 , [32] TG1 , [33] SW1 , [34] Clkout , [35] PLLIN ,
[LT1790-5.asy](#)

spice_model: LT1790-5

Required Refdes Prefix: X

spice_lib: LT1790.lib

Description: μ Power SOT-23 Low Dropout 5V Reference

Spice_Node_Sequence: [1] IN , [2] GND , [3] OUT ,
[LTC2902-2.asy](#)

spice_model: LTC2902-2

Required Refdes Prefix: X

spice_lib: LTC2902-2.sub

Description: Programmable Quad Supply Monitor with Adjustable Reset Timer and Supply Tolerance

Spice_Node_Sequence: [1] COMP3 , [2] COMP1 , [3] V3 , [4] V1 , [5] CRT , [6] _RST , [7] T0 , [8] _RDIS , [9] T1 , [10] GND , [11] VPG , [12] Vref , [13] V4 , [14] V2 , [15] COMP4 , [16] COMP2 ,
[LT1506-3.3.asy](#)

spice_model: LT1506-3.3

Required Refdes Prefix: X

spice_lib: LT1506.sub

Description: 4.5A, 500KHz Step-Down Switching Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] Vin , [2] Boost , [3] Sense , [4] GND , [5] Vc , [6] _SHDN , [7] SW ,
[LTC2900-1.asy](#)

spice_model: LTC2900-1

Required Refdes Prefix: X

spice_lib: LTC2900-1.sub

Description: Programmable Quad Supply Monitor with Adjustable Reset Timer

Spice_Node_Sequence: [1] V3 , [2] V1 , [3] CRT , [4] _RST , [5] _PBR , [6] GND , [7] VPG , [8] Vref , [9] V4 , [10] V2 ,
[LTC3221.asy](#)

spice_model: LTC3221

Required Refdes Prefix: X

spice_lib: LTC3221.sub

Description: μ Power, Regulated Charge Pump in 2x2 DFN

Spice_Node_Sequence: [1] C+ , [2] C- , [3] FB , [4] GND , [5] Vin , [6] Vout ,
[LTC2907.asy](#)

spice_model: LTC2907

Required Refdes Prefix: X

Description: Precision Dual Supply Monitor with One Pin-Selectable Threshold and One Adjustable Input

spice_lib: LTC2907.sub

Spice_Node_Sequence: [1] GND , [2] _RST , [3] TMR , [4] Vcc , [5] V1 , [6] Vadj , [7] S1 , [8] TOL ,
[LT1109.asy](#)

spice_model: LT1109

Required Refdes Prefix: X

spice_lib: LT1109.sub

Description: μ Power Low Cost DC/DC Converter, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vin , [2] SW , [3] GND , [4] _SDN , [5] FB ,

[LTC3412.asy](#)

spice_model: LTC3412

Required Refdes Prefix: X

spice_lib: LTC3412.sub

Description: 2.5A, 4MHz, Monolithic Synchronous Step-down Regulator

Spice_Node_Sequence: [1] Rt , [2] Mode , [3] SGND , [4] SW , [5] PGND , [6] PVin , [7] SVin , [8] Pgood , [9] FB , [10] Ith , [11] Run/SS ,

[LTC3809-1.asy](#)

spice_model: LTC3809-1

spice_lib: LTC3809-1.sub

Required Refdes Prefix: X

Description: No Rsense(TM), Low Input Voltage, Synchronous DC/DC Controller with Output Tracking
Note: Short circuit protection is not modeled.

Spice_Node_Sequence: [1] Mode , [2] Track/SS , [3] Vfb , [4] Ith , [5] Run , [6] Iprg , [7] BG , [8] TG , [9] Vin , [10] SW , [11] GND ,

[LTC3221-5.asy](#)

spice_model: LTC3221-5

Required Refdes Prefix: X

spice_lib: LTC3221-5.sub

Description: μ Power, Regulated Charge Pump in 2x2 DFN

Spice_Node_Sequence: [1] C+ , [2] C- , [3] _SHDN , [4] GND , [5] Vin , [6] Vout ,

[LT1963-1.5.asy](#)

spice_model: LT1963-1.5

Required Refdes Prefix: X

spice_lib: LT1963.lib

Description: 1.5A, Low Noise, Fast Transient Response LDO Regulator, Fixed 1.5V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] SENSE ,

[LTC3203B.asy](#)

spice_model: LTC3203B

Required Refdes Prefix: X

spice_lib: LTC3203B.sub

Description: 500mA Output Current Low Noise Dual Mode Step-up Charge Pump

Spice_Node_Sequence: [1] C2+ , [2] Vout , [3] C1+ , [4] _SHDN , [5] FB , [6] Mode , [7] Vin , [8] C2- , [9] GND , [10] C1- ,

[LTC1986.asy](#)

spice_model: LTC1986

Required Refdes Prefix: X

spice_lib: LTC1986.sub

Description: 3V/5V SIM Power Supply in SOT-23

Spice_Node_Sequence: [1] Vout , [2] GND , [3] MODE , [4] C- , [5] C+ , [6] Vin ,

[LT1584.asy](#)

spice_model: LT1584

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 7A Low Dropout Fast Response Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT3473.asy](#)

spice_model: LT3473

Required Refdes Prefix: X

spice_lib: LT3473.sub

Description: μ Power 1A Boost Converter with Schottky and Output Disconnect

Spice_Node_Sequence: [1] CAP , [2] OUT , [3] CTRL , [4] FB , [5] _Pgood , [6] _SHDN , [7] Vin , [8] SW , [9] GND ,

[LT3693.asy](#)

spice_model: LT3693

Required Refdes Prefix: X

spice_lib: LT3693.sub

Description: 38V, 3.5A, 2.4MHz Step-Down Switching Regulator

Spice_Node_Sequence: [1] BD , [2] Boost , [3] SW , [4] Vin , [5] Run/SS , [6] Pgood , [7] FB , [8] Vc , [9] Rt , [10] GND ,

[LTC1693-3.asy](#)

spice_model: LTC1693-3

Required Refdes Prefix: X

spice_lib: LTC1693-3.sub

Description: High Speed MOSFET Driver

Spice_Node_Sequence: [1] IN , [2] NC , [3] PHI , [4] GND , [5] NC , [6] NC , [7] OUT , [8] VCC ,

[LT1106.asy](#)

spice_model: LT1106

Required Refdes Prefix: X

spice_lib: LT1106.sub

Description: μ Power Step-Up DC/DC Converter for PCMCIA Card Flash Memory

Spice_Node_Sequence: [1] SEL 12/5 , [2] Soft Start , [3] Vcc , [4] 3/5 Detect , [5] PGND , [6] SW , [7] SGND , [8] _Vpp Valid , [9] G2 , [10] G1 , [11] Vpp2 , [12] Vpp1 , [13] AVpp , [14] ON/OFF ,

[LTC1778-1.asy](#)

spice_model: LTC1778-1

Required Refdes Prefix: X

spice_lib: LTC1778-1.sub

Description: Wide Operating Range, No Rsense(TM) Step-Down Controller

Spice_Node_Sequence: [1] Run/SS , [2] Von , [3] Vrmg , [4] FCB , [5] Ith , [6] GND , [7] Ion , [8] FB , [9] EXTVcc , [10] Vin , [11] INTVcc , [12] BG , [13] PGND , [14] SW , [15] TG , [16] Boost ,

[LT3743.asy](#)

spice_model: LT3743

Required Refdes Prefix: X

spice_lib: LT3743.sub

Description: High Current Synchronous Step-Down LED Driver with Three-State Control

Spice_Node_Sequence: [1] GND , [2] EN/UVLO , [3] Vref , [4] CTRL_T , [5] CTRL_H , [6] CTRL_L , [7] SS , [8] FB , [9] Sense+ , [10] Sense- , [11] VCL , [12] VCH , [13] Rt , [14] CTRL_SEL , [15] PWM , [16] PWMGH , [17] PWMGL , [18] HG , [19] SW , [20] CBOOT , [21] LG , [22] Vcc_int , [23] Vin ,

[LTC3901.asy](#)

spice_model: LTC3901

Required Refdes Prefix: X

spice_lib: LTC3901.sub

Description: Secondary Side Synchronous Driver for Push-Pull and Full-Bridge Converters

Spice_Node_Sequence: [1] PVcc , [2] ME , [3] PGND , [4] CSE- , [5] CSE+ , [6] TIMER , [7] GND , [8] SYNC , [9] CSF+ , [10] CSF- , [11] MF , [12] Vcc ,

[LT3013.asy](#)

spice_model: LT3013

Required Refdes Prefix: X

spice_lib: LT3013.lib

Description: 250mA, 4V to 80V Low Dropout μ Power Linear Regulator with PWRGD

Spice_Node_Sequence: [1] OUT , [2] ADJ , [3] GND , [4] PwrGD , [5] Ct , [6] _SHDN , [7] IN ,

[LTC3704.asy](#)

spice_model: LTC3704

Required Refdes Prefix: X

spice_lib: LTC3704.sub

Description: Wide Input Range, No Rsense(TM) Positive-to-Negative DC/DC Controller

Spice_Node_Sequence: [1] Run , [2] Ith , [3] NFB , [4] Freq , [5] Mode , [6] GND , [7] Gate , [8] INTVcc , [9] Vin , [10] Sense ,

[LTM4601HV.asy](#)

spice_model: LTM4601HV

Required Refdes Prefix: X

spice_lib: LTM4601.sub

Description: 12A DC/DC μ Module with PLL, Output Tracking and Margining

Spice_Node_Sequence: [1] Vin , [2] PGND , [3] Vout , [4] Vosns- , [5] Vout_LCL , [6] DiffVout , [7] Vosns+ , [8] SGND , [9] Pgood , [10] FB , [11] DRVcc , [12] MARG1 , [13] MARG0 , [14] Fset , [15] MPGM , [16] Comp , [17] Run , [18] Track/SS , [19] PLLIN , [20] INTVcc ,

[LT3695.asy](#)

spice_model: LT3695

Required Refdes Prefix: X

spice_lib: LT3695.sub

Description: 1A Fault Tolerant μ Power Step-Down Regulator

Spice_Node_Sequence: [1] PGND , [2] DA , [3] SW , [4] Run/SS , [5] Rt , [6] SYNC , [7] Vin , [8] Vc , [9] FB , [10] PG , [11] GND , [12] BD , [13] Boost ,

[LT1071HV.asy](#)

spice_model: LT1071HV

Required Refdes Prefix: X

spice_lib: LT1071.sub

Description: 2.5A High Efficiency Switching Regulator(High Voltage version)

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LTC3240-3.3.asy](#)

spice_model: LTC3240-3.3

Required Refdes Prefix: X

spice_lib: LTC3240-3.3.sub

Description: 3.3V Step-up Step-down Charge Pump DC/DC Converter

Spice_Node_Sequence: [1] GND , [2] Vin , [3] Vout , [4] C+ , [5] C- , [6] _SHDN ,

[LT1500-3.asy](#)

spice_model: LT1500-3/5

Required Refdes Prefix: X

spice_lib: LT1500-3.sub

Description: Adaptive-Frequency Current-Mode Switching Regulator, Selectable 3.3V or 5V Output

Spice_Node_Sequence: [1] _SHDN , [2] Vc , [3] Vin , [4] Isense , [5] GND , [6] PGND , [7] SW , [8] SYNC , [9] LBO , [10] LBI , [11] SS , [12] SEL3.3/5 , [13] Sense ,

[LT1372.asy](#)

spice_model: LT1372

Required Refdes Prefix: X

spice_lib: LT1372.sub

Description: 1.5A, 500kHz High Efficiency Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vc , [2] FB , [3] NFB , [4] _SHDN , [5] Vin , [6] GND , [7] SW ,

[LT1339.asy](#)

spice_model: LT1339

Required Refdes Prefix: X

spice_lib: LT1339.sub

Description: High Power Synchronous DC/DC Controller

Spice_Node_Sequence: [1] 5Vref , [2] Ct , [3] SL/ADJ , [4] Iave , [5] SS , [6] Vc , [7] SGND , [8] FB , [9]

Sense+ , [10] Sense- , [11] Run/SHDN , [12] Phase , [13] PGND , [14] BG , [15] 12Vin , [16] TS , [17] TG , [18] Boost ,

[LT1071.asy](#)

spice_model: LT1071

Required Refdes Prefix: X

spice_lib: LT1071.sub

Description: 2.5A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LT3751.asy](#)

spice_model: LT3751

Required Refdes Prefix: X

spice_lib: LT3751.sub

Description: High Voltage Capacitor Charger Controller with Regulation

Spice_Node_Sequence: [1] RVtrans , [2] UVLO1 , [3] OVLO1 , [4] UVLO2 , [5] OVLO2 , [6] _Fault , [7]

_Done , [8] Charge , [9] Clamp , [10] FB , [11] CSN , [12] CSP , [13] Vcc , [14] LVGate , [15] HVGate ,

[16] RBG , [17] RVout , [18] RDCM , [19] GND ,

[LT1767-2.5.asy](#)

spice_model: LT1767-2.5

Required Refdes Prefix: X

spice_lib: LT1767-x.x.sub

Description: Monolithic 1.5A, 1.25MHz Step-Down Switching Regulator, Fixed 2.5V Output\n\nNote: Sync pin is not modeled.

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] GND , [5] _SHDN , [6] FB , [7] Vc ,

[LT1965B.asy](#)

spice_model: LT1965B

Required Refdes Prefix: X

spice_lib: LT1965.lib

Description: 1.1A, Low Noise, Low Dropout Linear Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] ADJ ,

[LT1300.asy](#)

spice_model: LT1300

Required Refdes Prefix: X

spice_lib: LT1300.sub

Description: μ Power High Efficiency 3.3/5V Step-Up DC/DC Converter

Spice_Node_Sequence: [1] GND , [2] Sel , [3] SHDN , [4] FB , [5] Ilim , [6] Vin , [7] SW , [8] PGND ,

[LTC3801.asy](#)

spice_model: LTC3801

Required Refdes Prefix: X

spice_lib: LTC3801.sub

Description: μ Power Constant Frequency Step-Down DC/DC Controller in ThinSOT

Spice_Node_Sequence: [1] Ith/Run , [2] GND , [3] FB , [4] Sense- , [5] Vin , [6] Pgate ,

[LTC3561A.asy](#)

spice_model: LTC3561A

Required Refdes Prefix: X

spice_lib: LTC3561A.sub

Description: 1A, 4MHz, Synchronous Step-down DC/DC Converter

Spice_Node_Sequence: [1] SHDN/Rt , [2] SGND , [3] SW , [4] PGND , [5] PVin , [6] SVin , [7] FB , [8]

Ith ,

[LTC1982.asy](#)

spice_model: LTC1982

Required Refdes Prefix: X

spice_lib: LTC1982.sub

Description: Dual μ Power High Side Switch Controllers in SOT-23

Spice_Node_Sequence: [1] _SHDN1 , [2] GND , [3] _SHDN2 , [4] Gate2 , [5] Gate1 , [6] Vcc ,
[LT3435.asy](#)

spice_model: LT3435

Required Refdes Prefix: X

spice_lib: LT3435.sub

Description: High Voltage 3A, 500KHz Step-Down Switching Regulator with 100 μ A Quiescent Current

Spice_Node_Sequence: [1] SW , [2] Vin , [3] Boost , [4] CT , [5] GND , [6] Css , [7] VBIAS , [8] Vc , [9] FB , [10] PGFB , [11] _SHDN , [12] Pgood ,

[LT1310.asy](#)

spice_model: LT1310

Required Refdes Prefix: X

spice_lib: LT1310.sub

Description: 1.5A Boost DC/DC Converter with Phase-Locked Loop

Spice_Node_Sequence: [1] FB , [2] _SHDN , [3] PLL-LPF , [4] SYNC , [5] GND , [6] SW , [7] Vin , [8] Ct , [9] Vc ,

[LT1764-3.3.asy](#)

spice_model: LT1764-3.3

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,

[LT3508.asy](#)

spice_model: LT3508

Required Refdes Prefix: X

spice_lib: LT3508.sub

Description: Dual Monolithic 1.4A Step-Down Switching Regulator

Spice_Node_Sequence: [1] Boost1 , [2] _SHDN , [3] SW1 , [4] Vin , [5] Rt , [6] SW2 , [7] GND , [8] Boost2 , [9] FB2 , [10] Vc2 , [11] Pgood2 , [12] Track/SS2 , [13] Track/SS1 , [14] Pgood1 , [15] Vc1 , [16] FB1 ,

[LT3485-2.asy](#)

spice_model: LT3485-2

Required Refdes Prefix: X

spice_lib: LT3485-2.sub

Description: Photoflash Capacitor Charger with Output Voltage Monitor and Integrated IGBT Drive -- 850mA typ Peak Switch Current

Spice_Node_Sequence: [1] Charge , [2] Vbat , [3] Vin , [4] SW , [5] IBGTout , [6] IBGTin , [7] IBGTpwr , [8] _Done , [9] Vmont , [10] GND ,

[LTC3611.asy](#)

spice_model: LTC3611

Required Refdes Prefix: X

spice_lib: LTC3611.sub

Description: 10A, 32V Monolithic Synchronous Step-Down DC/DC Converter

Spice_Node_Sequence: [1] PGND , [2] SW , [3] Vin , [4] GND , [5] Boost , [6] Run/SS , [7] Von , [8] Pgood , [9] Vrng , [10] Ith , [11] FCB , [12] Ion , [13] FB , [14] EXTVcc , [15] INTVcc ,

[LT1076-5.asy](#)

spice_model: LT1076-5

Required Refdes Prefix: X

spice_lib: LT1076-5.sub

Description: 2A Step-Down Switching Regulator

Spice_Node_Sequence: [1] Vin , [2] SW , [3] Ilim , [4] GND , [5] Sense , [6] Vc , [7] SHDN ,

[LTC4252A-2.asy](#)

spice_model: LTC4252A-2

Required Refdes Prefix: X

spice_lib: LTC4252A-2.sub

Description: Negative Voltage Hot Swap Controller

Spice_Node_Sequence: [1] Vin , [2] _PWRGD , [3] SS , [4] Sense , [5] Vee , [6] Gate , [7] Drain , [8] OV , [9] UV , [10] Timer ,

[LTC1430A.asy](#)

spice_model: LTC1430A

Required Refdes Prefix: X

spice_lib: LTC1430A.sub

Description: High Power Step-Down Synchronous Switching Regulator Controller

Spice_Node_Sequence: [1] G1 , [2] PVcc1 , [3] PGND , [4] SGND , [5] Sense- , [6] FB , [7] Sense+ , [8] _SHDN , [9] SS , [10] Comp , [11] Freq , [12] Imax , [13] Ifb , [14] Vcc , [15] PVcc2 , [16] G2 ,

[LTC1706-63.asy](#)

spice_model: LTC1706-63

Required Refdes Prefix: X

spice_lib: LTC1706-63.sub

Description: 5-Bit VID Voltage Programmer for Sun CPUs

Spice_Node_Sequence: [1] VID2 , [2] VID3 , [3] Vcc , [4] GND , [5] FB , [6] Sense , [7] VID0 , [8] VID1 , [9] VID4 ,

[LTC3709.asy](#)

spice_model: LTC3709

Required Refdes Prefix: X

spice_lib: LTC3709.sub

Description: Fast 2-Phase, No Rsense, Synchronous DC/DC Controller with Tracking/Sequencing

Spice_Node_Sequence: [1] RUN/SS , [2] Ith , [3] FB , [4] Track , [5] SGND , [6] Vos- , [7] DiffOut , [8] Vos+ , [9] ExtLPF , [10] IntLPF , [11] Boost2 , [12] TG2 , [13] SW2 , [14] Sense2+ , [15] Vcc , [16] Sense2- , [17] PGND2 , [18] BG2 , [19] DRVcc , [20] BG1 , [21] PGND1 , [22] Sense1- , [23] Sense1+ , [24] SW1 , [25] TG1 , [26] Boost1 , [27] Pgood , [28] Ion , [29] FCB , [30] Vrng ,

[LT3468.asy](#)

spice_model: LT3468

Required Refdes Prefix: X

spice_lib: LT3468.sub

Description: Photoflash Capacitor Charger in ThinSOT(TM)

Spice_Node_Sequence: [1] SW , [2] GND , [3] _Done , [4] Charge , [5] Vin ,

[LT3500.asy](#)

spice_model: LT3500

Required Refdes Prefix: X

spice_lib: LT3500.sub

Description: Monolithic 2A Step-Down Regulator Plus Linear Regulator/Controller

Spice_Node_Sequence: [1] Vin , [2] _SHDN , [3] SS , [4] Pgood , [5] Vc , [6] Rt , [7] _Pgood , [8] FB , [9] LFB , [10] LDRV , [11] Boost , [12] SW , [13] GND ,

[LTC3127.asy](#)

spice_model: LTC3127

Required Refdes Prefix: X

spice_lib: LTC3127.sub

Description: 1A Buck-Boost DC/DC Converter with Programmable Input Current Limit

Spice_Node_Sequence: [1] PGND , [2] SW1 , [3] Vin , [4] _SHDN , [5] Mode , [6] PROG , [7] SGND , [8] FB , [9] Vc , [10] Vout , [11] SW2 ,

[LTC3444.asy](#)

spice_model: LTC3444

Required Refdes Prefix: X

spice_lib: LTC3444.sub

Description: μ Power Synchronous Buck-Boost DC/DC Converter for WCDMA Applications

Spice_Node_Sequence: [1] _SHDN , [2] SW1 , [3] GND , [4] SW2 , [5] Vout , [6] Vin , [7] Vc , [8] FB ,

[LTC3609.asy](#)

spice_model: LTC3609

Required Refdes Prefix: X

spice_lib: LTC3609.sub

Description: 6A, 32V Monolithic Synchronous Step-Down DC/DC Converter

Spice_Node_Sequence: [1] PGND , [2] SW , [3] Vin , [4] GND , [5] Boost , [6] Run/SS , [7] Von , [8] Pgood , [9] Vrng , [10] Ith , [11] FCB , [12] Ion , [13] FB , [14] EXTVcc , [15] INTVcc ,

[LTM8021.asy](#)

spice_model: LTM8021

Required Refdes Prefix: X

spice_lib: LTM8021.sub

Description: 500mA, 40V DC/DC μ Module

Spice_Node_Sequence: [1] Vin , [2] _SHDN , [3] GND , [4] ADJ , [5] BIAS , [6] OUT ,

[LTC3414.asy](#)

spice_model: LTC3414

Required Refdes Prefix: X

spice_lib: LTC3414.sub

Description: 4A, 4MHz, Monolithic Synchronous Step-down Regulator

Spice_Node_Sequence: [1] Rt , [2] Mode , [3] SGND , [4] SW , [5] PGND , [6] PVin , [7] SVin , [8] Pgood , [9] FB , [10] Ith , [11] Run/SS ,

[LTC3703.asy](#)

spice_model: LTC3703

Required Refdes Prefix: X

spice_lib: LTC3703.sub

Description: 100V High Voltage Switching Regulator Controller

Spice_Node_Sequence: [1] Mode , [2] Fset , [3] Comp , [4] FB , [5] Imax , [6] INV , [7] Run/SS , [8] GND , [9] BGRTN , [10] BG , [11] DRVcc , [12] VCC , [13] SW , [14] TG , [15] Boost , [16] Vin ,

[LTC1174HV.asy](#)

spice_model: LTC1174HV

Required Refdes Prefix: X

spice_lib: LTC1174.sub

Description: High Efficiency Step-Down and Inverting DC/DC Converter, Adjustable Output Voltage

Spice_Node_Sequence: [1] FB , [2] GND , [3] SW , [4] Vin , [5] Ipgm , [6] _SHDN , [7] LBin , [8] LBout ,

[LTC1706-61.asy](#)

spice_model: LTC1706-61

Required Refdes Prefix: X

spice_lib: LTC1706-61.sub

Description: 5-Bit VID Voltage Programmer for AMD Opteron CPUs

Spice_Node_Sequence: [1] VID2 , [2] VID3 , [3] Vcc , [4] GND , [5] FB , [6] Sense , [7] VID0 , [8] VID1 , [9] VID4 ,

[LTC3707-SYNC.asy](#)

spice_model: LTC3707-SYNC

Required Refdes Prefix: X

spice_lib: LTC1628-SYNC.sub

Description: High Efficiency, 2-Phase Synchronous Step-Down Switching Regulator

Spice_Node_Sequence: [1] Run/SS1 , [2] Sense1+ , [3] Sense1- , [4] Vosense1 , [5] PLLFLTR , [6] PLLIN , [7] FCB , [8] Ith1 , [9] SGND , [10] 3.3Vout , [11] Ith2 , [12] Vosense2 , [13] Sense2- , [14] Sense2+ , [15] Run/SS2 , [16] TG2 , [17] SW2 , [18] Boost2 , [19] BG2 , [20] PGND , [21] INTVcc , [22] EXTVcc , [23] BG1 , [24] Vin , [25] Boost1 , [26] SW1 , [27] TG1 , [28] Pgood ,

[LTC3723-1.asy](#)

spice_model: LTC3723-1

Required Refdes Prefix: X

spice_lib: LTC3723-1.sub

Description: Synchronous Push-Pull PWM Controller\n\nNote: hiccup mode fault logic is not modeled.
Spice_Node_Sequence: [1] DRVb , [2] Vcc , [3] DRVA , [4] SDRA , [5] GND , [6] Ct , [7] Sprog , [8] Dprg , [9] CS , [10] COMP , [11] Rleb , [12] FB , [13] SS , [14] UVLO , [15] Vref , [16] SDRB ,
[LT1509.asy](#)

spice_model: LT1509

Required Refdes Prefix: X

spice_lib: LT1509.sub

Description: Power Factor and PWM Controller

Spice_Node_Sequence: [1] GTDR1 , [2] GND2 , [3] GND1 , [4] Cset , [5] PKlim , [6] CAout , [7] Isense , [8] Mout , [9] Iac , [10] VAout , [11] OVP , [12] REF , [13] SS2 , [14] Vsense , [15] Rset , [16] SS1 , [17] Vcc , [18] Vc , [19] RAMP , [20] GTDR2 ,

[LTC3545-1.asy](#)

spice_model: LTC3545-1

Required Refdes Prefix: X

Description: Triple 800mA Synchronous Step-Down Regulator - 2.25MHz

spice_lib: LTC3545-1.sub

Spice_Node_Sequence: [1] SW1 , [2] Pgood1 , [3] Run2 , [4] Pgood2 , [5] SW2 , [6] PGND , [7] PVin , [8] SW3 , [9] Pgood3 , [10] Run3 , [11] FB3 , [12] FB2 , [13] FB1 , [14] Run1 , [15] Vin , [16] GNDA ,

[LT1269.asy](#)

spice_model: LT1269

Required Refdes Prefix: X

spice_lib: LT1269.sub

Description: 4A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LTC3453.asy](#)

spice_model: LTC3453

Required Refdes Prefix: X

spice_lib: LTC3453.sub

Description: Synchronous Buck-Boost High Power White LED Driver

Spice_Node_Sequence: [1] Vin , [2] EN1 , [3] Iset1 , [4] LED1 , [5] GND , [6] LED2 , [7] LED3 , [8] LED4 , [9] Iset2 , [10] EN2 , [11] Vc , [12] Vout , [13] SW2 , [14] SW1 , [15] PVin ,

[LT1786F.asy](#)

spice_model: LT1786F

Required Refdes Prefix: X

spice_lib: LT1786F.sub

Description: SMBus Programmable CCFL Switching Regulator(Bits-to-Nits(TM))

Spice_Node_Sequence: [1] PGND , [2] Iccfl , [3] DIO , [4] CCFL Vc , [5] AGND , [6] _SHDN , [7] _SMBSUS , [8] ADR , [9] SDA , [10] SCL , [11] Iout , [12] Vcc , [13] ROYER , [14] BAT , [15] BULB , [16] CCFL SW ,

[LT3020-1.8.asy](#)

spice_model: LT3020-1.8

Required Refdes Prefix: X

spice_lib: LT3020.lib

Description: 100mA, Low Voltage, Very Low Dropout Linear Regulator, Fixed 1.8V Output

Spice_Node_Sequence: [1] OUT , [2] GND , [3] _SHDN , [4] IN ,

[LTC3528-2.asy](#)

spice_model: LTC3528-2

Required Refdes Prefix: X

spice_lib: LTC3528-2.sub

Description: 1A, 2MHz Synchronous Step-up DC/DC Converter in 2mm×3mm DFN

Spice_Node_Sequence: [1] _SHDN , [2] FB , [3] Pgood , [4] Vout , [5] SW , [6] PGND , [7] SGND , [8] Vin ,

[LT1962-2.5.asy](#)

spice_model: LT1962-2.5

Required Refdes Prefix: X

spice_lib: LT1962.lib

Description: 300mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 2.5V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN ,

[LT1584-3.6.asy](#)

spice_model: LT1584-3.6

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 7A Low Dropout Fast Response Positive Regulator, Fixed 3.6V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LTC3402.asy](#)

spice_model: LTC3402

Required Refdes Prefix: X

spice_lib: LTC3402.sub

Description: 2A, 3MHz μ Power Synchronous Boost Converter

Spice_Node_Sequence: [1] Rt , [2] MODE , [3] Vin , [4] SW , [5] GND , [6] Pgood , [7] Vout , [8] FB , [9]

Vc , [10] _SHDN ,

[LT3685.asy](#)

spice_model: LT3685

Required Refdes Prefix: X

spice_lib: LT3685.sub

Description: 38V, 2A, 2.4MHz Step-Down Switching Regulator

Spice_Node_Sequence: [1] BD , [2] Boost , [3] SW , [4] Vin , [5] Run/SS , [6] Pgood , [7] FB , [8] Vc , [9]

Rt , [10] GND ,

[LTC1474-5.asy](#)

spice_model: LTC1474-5

Required Refdes Prefix: X

spice_lib: LTC1474.sub

Description: Low Quiescent Current, High Efficiency Step-Down Converter, 5V Output

Spice_Node_Sequence: [1] FB , [2] LBO , [3] LBI , [4] GND , [5] SW , [6] Sense , [7] Vin , [8] Run ,

[LT1949-1.asy](#)

spice_model: LT1949-1

Required Refdes Prefix: X

spice_lib: LT1949-1.sub

Description: 1.1MHz, 1A PWM DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] GND , [5] SW , [6] Vin , [7] LBI , [8] LBO ,

[LT1963-1.8.asy](#)

spice_model: LT1963-1.8

Required Refdes Prefix: X

spice_lib: LT1963.lib

Description: 1.5A, Low Noise, Fast Transient Response LDO Regulator, Fixed 1.8V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] SENSE ,

[LT1084.asy](#)

spice_model: LT1084

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 5A Low Dropout Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LTC1438-ADJ.asy](#)

spice_model: LTC1438-ADJ

Required Refdes Prefix: X

spice_lib: LTC1438-ADJ.sub

Description: Dual High Efficiency, Low Noise, Synchronous Step-Down Switching Regulator

Spice_Node_Sequence: [1] Run/SS1 , [2] Sense+1 , [3] Sense-1 , [4] Vsense1 , [5] Ith1 , [6] Cosc , [7] SGND , [8] SFB1 , [9] Ith2 , [10] Vosense2 , [11] Sense-2 , [12] Sense+2 , [13] Run/SS2 , [14] AUXDR , [15] AUXFB , [16] AUXON , [17] Boost2 , [18] TGL2 , [19] SW2 , [20] EXTVcc , [21] BG2 , [22] PGND , [23] INTVcc , [24] BG1 , [25] Vin , [26] SW1 , [27] TGL1 , [28] Boost1 ,

[LTC3528B-2.asy](#)

spice_model: LTC3528B-2

Required Refdes Prefix: X

spice_lib: LTC3528B-2.sub

Description: 1A, 2MHz Synchronous Step-up DC/DC Converter in 3mm×2mm DFN

Spice_Node_Sequence: [1] _SHDN , [2] FB , [3] Pgood , [4] Vout , [5] SW , [6] PGND , [7] SGND , [8] Vin ,

[LT3585-1.asy](#)

spice_model: LT3585-1

Required Refdes Prefix: X

spice_lib: LT3585-1.sub

Description: Photoflash Capacitor Charger with Adjustable Input Current and IGBT Driver

Spice_Node_Sequence: [1] CHRGIadj , [2] Vbat , [3] Vin , [4] SW , [5] IBGTPU , [6] IBGTPD , [7] IBGTin , [8] IBGTpwr , [9] _Done , [10] GND ,

[LT3021.asy](#)

spice_model: LT3021

Required Refdes Prefix: X

spice_lib: LT3021.lib

Description: 500mA, Low Voltage, Very Low Dropout Linear Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] OUT , [2] ADJ , [3] _SHDN , [4] GND , [5] IN ,

[LT1620S8.asy](#)

spice_model: LT1620S8

Required Refdes Prefix: X

spice_lib: LT1620S8.sub

Description: Rail-to-Rail Current Sense Amplifier

Spice_Node_Sequence: [1] Sense , [2] Iout , [3] GND , [4] IN- , [5] IN+ , [6] Vcc , [7] PROG , [8] AVE ,

[LT3022-1.5.asy](#)

spice_model: LT3022-1.5

Required Refdes Prefix: X

spice_lib: LT3022.lib

Description: 1A, 0.9V to 10V, Very Low Dropout Linear Regulator, Fixed 1.5V Output Voltage

Spice_Node_Sequence: [1] OUT , [2] ADJ , [3] _SHDN , [4] GND , [5] IN ,

[LTC3642.asy](#)

Required Refdes Prefix: X

spice_lib: LTC3642.sub

spice_model: LTC3642

Description: High Efficiency, High Voltage 50mA Synchronous Step-Down Converter

Spice_Node_Sequence: [1] SW , [2] Vin , [3] Iset , [4] SS , [5] Run , [6] FB , [7] Hyst , [8] GND ,

[LT3494.asy](#)

spice_model: LT3494

Required Refdes Prefix: X

spice_lib: LT3494.sub

Description: μ Power Low Noise Boost Converter with Output Disconnect

Spice_Node_Sequence: [1] SW , [2] GND , [3] Vcc , [4] CTRL , [5] _SHDN , [6] FB , [7] Vout , [8] CAP ,

[LTC3522.asy](#)

spice_mode

[phidet.asy](#)

Required Refdes Prefix: A

spice_lib: PHASEDET

Description: Behavioral Type 3/4 Phase Detector (phase/frequency detector). NOTE: Limit input rise times

Spice_Node_Sequence: [1] a , [2] b , [3] Q , [4] com ,

[xor.asy](#)

Required Refdes Prefix: A

spice_lib: XOR

Description: Behavioral XOR gate

Spice_Node_Sequence: [1] a , [2] b , [3] c , [4] d , [5] e , [6] _Q , [7] Q , [8] com ,

[dflop.asy](#)

Required Refdes Prefix: A

spice_lib: DFLOP

Description: Behavioral D-flipflop

Spice_Node_Sequence: [1] D , [2] CLK , [3] PRE , [4] CLR , [5] _Q , [6] Q , [7] com ,

[schmitt.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered buffer with complementary outs

Spice_Node_Sequence: [1] in , [2] _Q , [3] Q , [4] com ,

[schmtinv.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered inverter

Spice_Node_Sequence: [1] in , [2] _Q , [3] com ,

[inv.asy](#)

Required Refdes Prefix: A

spice_lib: BUF

Description: Behavioral Inverter

Spice_Node_Sequence: [1] in , [2] _Q , [3] com ,

[schmtbuf.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered buffer

Spice_Node_Sequence: [1] in , [2] Q , [3] com ,

[buf.asy](#)

Required Refdes Prefix: A

spice_lib: BUF

Description: Behavioral buffer with complementary outputs

Spice_Node_Sequence: [1] in , [2] _Q , [3] Q , [4] com ,

[buf1.asy](#)

Required Refdes Prefix: A

spice_lib: BUF

Description: Behavioral buffer

Spice_Node_Sequence: [1] in , [2] Q , [3] com ,

[diffschmtinv.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered Inverter with Differential Input

Spice_Node_Sequence: [1] in+ , [2] in- , [3] _Q , [4] com ,

[diffschmitt.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered Buffer with Complementary Outs and Differential Input

Spice_Node_Sequence: [1] in+ , [2] in- , [3] _Q , [4] Q , [5] com ,

[diffschmtbuf.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered Buffer with Differential Input

Spice_Node_Sequence: [1] in+ , [2] in- , [3] Q , [4] com ,

[and.asy](#)

Required Refdes Prefix: A

spice_lib: AND

Description: Behavioral AND gate

Spice_Node_Sequence: [1] a , [2] b , [3] c , [4] d , [5] e , [6] _Q , [7] Q , [8] com ,

[srflop.asy](#)

Required Refdes Prefix: A

spice_lib: SRFLOP

Description: Behavioral Set-Reset Flipflop

Spice_Node_Sequence: [1] S , [2] R , [3] _Q , [4] Q , [5] com ,

[or.asy](#)

Required Refdes Prefix: A

spice_lib: OR

Description: Behavioral OR gate with complementary outputs

Spice_Node_Sequence: [1] a , [2] b , [3] c , [4] d , [5] e , [6] _Q , [7] Q , [8] com ,

[LT6700-3.asy](#)

spice_model: LT6700-3

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power, Low Voltage, SOT-23, Dual Comparator with 400mV Reference

Spice_Node_Sequence: [1] OUTA , [2] GND , [3] +INA , [4] +INB , [5] Vs , [6] OUTB ,

[LT1016.asy](#)

spice_model: LT1016

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra Fast Precision Comparator

Spice_Node_Sequence: [1] V+ , [2] In+ , [3] In- , [4] V- , [5] LATCH , [6] GND , [7] OUT+ , [8] OUT- ,

[LT1017.asy](#)

spice_model: LT1017

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power Dual Comparator(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC1841.asy](#)

spice_model: LTC1841

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Ultralow Power Dual Comparator(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC1540.asy](#)

spice_model: LTC1540

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Nanopower Comparator with Reference

Spice_Node_Sequence: [1] GND , [2] V- , [3] In+ , [4] In- , [5] HYST , [6] REF , [7] V+ , [8] OUT ,
[LTC1443.asy](#)

spice_model: LTC1443

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] OUT , [2] V+ , [3] In- , [4] In+ , [5] REF , [6] V- , [7] GND ,
[LT1715.asy](#)

spice_model: LT1715

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: 7ns, Low Power, 3V/5V/±5V Rail-to-Rail Comparator

Spice_Node_Sequence: [1] In+ , [2] In- , [3] Vcc , [4] Vee , [5] VS+ , [6] GND , [7] OUT ,
[LT1711.asy](#)

spice_model: LT1711

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: 4.5ns, 3V/5V/±5V Rail-to-Rail Comparator

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] GND , [6] OUT+ , [7] OUT- , [8] LATCH ,
[LT1015.asy](#)

spice_model: LT1015

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: High Speed Dual Line Receiver(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] VCC , [4] VEE , [5] OUT+ , [6] LATCH ,
[LT1394.asy](#)

spice_model: LT1394

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 7ns, Low Power Single Supply, Ground-Sensing Comparator

Spice_Node_Sequence: [1] V+ , [2] In+ , [3] In- , [4] V- , [5] LE , [6] GND , [7] Q , [8] _Q ,
[LTC1842.asy](#)

spice_model: LTC1842

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Ultralow Power Dual Comparator with Reference

Spice_Node_Sequence: [1] OUTA , [2] GND , [3] INA+ , [4] INB+ , [5] Hyst , [6] Ref , [7] Vcc , [8] OUTB ,
[LTC1445.asy](#)

spice_model: LTC1445

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] OUT , [2] V+ , [3] In- , [4] In+ , [5] REF , [6] V- , [7] HYST ,
[LT1011.asy](#)

spice_model: LT1011

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Voltage Comparator

Spice_Node_Sequence: [1] GND , [2] In+ , [3] In- , [4] V- , [5] BAL , [6] STROBE , [7] OUT , [8] V+ ,
[LT1720.asy](#)

spice_model: LT1720

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 4.5ns, Single Supply 3V/5V Comparator with Rail-to-Rail Outputs

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6700-2.asy](#)

spice_model: LT6700-2

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power, Low Voltage, SOT-23, Dual Comparator with 400mV Reference

Spice_Node_Sequence: [1] OUTA , [2] GND , [3] -INA , [4] -INB , [5] Vs , [6] OUTB ,

[LTC1444.asy](#)

spice_model: LTC1444

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] OUT , [2] V+ , [3] In- , [4] In+ , [5] REF , [6] V- , [7] HYST ,

[LTC1042.asy](#)

spice_model: LTC1042

Required Refdes Prefix: X

spice_lib: LTC1042.sub

Description: Window Comparator

Spice_Node_Sequence: [1] Within , [2] Center , [3] In , [4] GND , [5] Width/2 , [6] Above , [7] OSC , [8] Vcc ,

[LT6703-3.asy](#)

spice_model: LT6703-3

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: μ Power, Low Voltage, SOT-23, Dual Comparator with 400mV Reference

Spice_Node_Sequence: [1] OUT , [2] GND , [3] +IN , [4] Vs ,

[LTC1440.asy](#)

spice_model: LTC1440

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] GND , [7] REF , [8] HYST ,

[LTC1041.asy](#)

spice_model: LTC1041

Required Refdes Prefix: X

spice_lib: LTC1041.sub

Description: BANG-BANG Controller

Spice_Node_Sequence: [1] ON/_OFF , [2] Vin , [3] SetPnt , [4] GND , [5] Delta , [6] OSC , [7] Vpp , [8] Vcc ,

[LT6703-2.asy](#)

spice_model: LT6703-2

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: μ Power, Low Voltage, SOT-23, Dual Comparator with 400mV Reference

Spice_Node_Sequence: [1] OUT , [2] GND , [3] -IN , [4] Vs ,

[LT1018.asy](#)

spice_model: LT1018

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power Dual Comparator(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1671.asy](#)

spice_model: LT1671

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 60ns, Low Power, Single Supply, Ground-Sensing Comparator

Spice_Node_Sequence: [1] V+ , [2] In+ , [3] In- , [4] V- , [5] LE , [6] GND , [7] Q , [8] _Q ,

[LT6700-1.asy](#)

spice_model: LT6700-1

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power, Low Voltage, SOT-23, Dual Comparator with 400mV Reference

Spice_Node_Sequence: [1] OUTA , [2] GND , [3] +INA , [4] -INB , [5] Vs , [6] OUTB ,

[LTC1442.asy](#)

spice_model: LTC1442

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] OUT A , [2] V- , [3] InA+ , [4] InB- , [5] HYST , [6] Ref , [7] V+ , [8] OUT B ,

[LTC1843.asy](#)

spice_model: LTC1843

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Ultralow Power Dual Comparator with Reference

Spice_Node_Sequence: [1] OUTA , [2] GND , [3] INA+ , [4] INB- , [5] Hyst , [6] Ref , [7] Vcc , [8] OUTB

,
[LTC6702.asy](#)

spice_model: LTC6702

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Tiny μ Power Low Voltage Comparator

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] GND , [5] OUT ,

[LT1713.asy](#)

spice_model: LT1713

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: 7ns, Low Power, 3V/5V/ \pm 5V Rail-to-Rail Comparator

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] GND , [6] OUT+ , [7] OUT- , [8] LATCH ,

[LTC1441.asy](#)

spice_model: LTC1441

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] OUT , [2] V- , [3] In+ , [4] In- , [5] V+ ,

[LT1716.asy](#)

spice_model: LT1716

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: SOT-23, 44V, Over-the-Top, μ Power, Precision, Rail-to-Rail Comparator

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1719.asy](#)

spice_model: LT1719

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 4.5ns, Single Supply 3V/5V Comparator with Rail-to-Rail Outputs

Spice_Node_Sequence: [1] V+ , [2] In+ , [3] In- , [4] V- , [5] GND , [6] SHDN , [7] OUT , [8] VS+ ,
[LT1712.asy](#)

spice_model: LT1712

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Dual 4.5ns, 3V/5V/±5V Rail-to-Rail Comparator(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] GND , [6] OUT+ , [7] OUT- , [8] LATCH ,
[LT1721.asy](#)

spice_model: LT1721

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 4.5ns, Single Supply 3V/5V Comparator with Rail-to-Rail Outputs

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1714.asy](#)

spice_model: LT1714

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Dual 7ns, Low Power, 3V/5V/±5V Rail-to-Rail Comparator(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] GND , [6] OUT+ , [7] OUT- , [8] LATCH ,
[LT1116.asy](#)

spice_model: LT1116

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 12ns, Single Supply Ground-Sensing Comparator

Spice_Node_Sequence: [1] V+ , [2] In+ , [3] In- , [4] V- , [5] LATCH , [6] GND , [7] OUT+ , [8] OUT- ,

[4N27.asy](#)

spice_model: 4N27

Required Refdes Prefix: X

spice_lib: 4N27.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,
[MOC206.asy](#)

spice_model: MOC206

Required Refdes Prefix: X

spice_lib: MOC205.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,
[PC817A.asy](#)

spice_model: PC817A

Required Refdes Prefix: X

spice_lib: PC817.sub

Description: High Density Mounting Type Photocoupler

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E ,
[CNY17-1.asy](#)

spice_model: CNY17-1

Required Refdes Prefix: X

spice_lib: CNY17.sub

Description: Optoisolator, Transistor Output
Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,
[4N28.asy](#)
spice_model: 4N28
Required Refdes Prefix: X
spice_lib: 4N27.sub
Description: Optoisolator, Transistor Output
Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,
[MOC205.asy](#)
spice_model: MOC205
Required Refdes Prefix: X
spice_lib: MOC205.sub
Description: Optoisolator, Transistor Output
Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,
[PC817C.asy](#)
spice_model: PC817C
Required Refdes Prefix: X
spice_lib: PC817.sub
Description: High Density Mounting Type Photocoupler
Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E ,
[MOC207.asy](#)
spice_model: MOC207
Required Refdes Prefix: X
spice_lib: MOC205.sub
Description: Optoisolator, Transistor Output
Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,
[CNY17-2.asy](#)
spice_model: CNY17-2
Required Refdes Prefix: X
spice_lib: CNY17.sub
Description: Optoisolator, Transistor Output
Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,
[4N26.asy](#)
spice_model: 4N26
Required Refdes Prefix: X
spice_lib: 4N25.sub
Description: Optoisolator, Transistor Output
Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,
[4N25.asy](#)
spice_model: 4N25
Required Refdes Prefix: X
spice_lib: 4N25.sub
Description: Optoisolator, Transistor Output
Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,
[PC817B.asy](#)
spice_model: PC817B
Required Refdes Prefix: X
spice_lib: PC817.sub
Description: High Density Mounting Type Photocoupler
Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E ,
[4N25A.asy](#)
spice_model: 4N25A
Required Refdes Prefix: X

spice_lib: 4N25.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,
[PC817D.asy](#)

spice_model: PC817D

Required Refdes Prefix: X

spice_lib: PC817.sub

Description: High Density Mounting Type Photocoupler

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E ,
[CNY17-3.asy](#)

spice_model: CNY17-3

Required Refdes Prefix: X

spice_lib: CNY17.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,